# UITAL WP2101 UItra-Small Load Switch with Slew Rate Control

# **1. General Description**

The WP2101 is a single channel P-channel MOSFET load switch with output slew rate control. This switch operates with wide inputs ranging from 1.2V to 5.5V. The device features output slew rate control, limiting inrush currents during turn on to protect downstream devices.

The WP2101 series has 4 versions (A/B/C/D) according with EN high active or low active and reverse block function is or not.

The WP2101 is available in Pb-free packages and is characterized for operation over the free-air temperature range of -  $40^{\circ}$ C to  $85^{\circ}$ C.

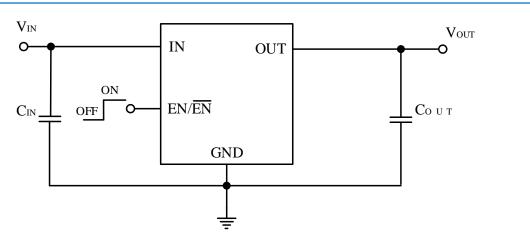
### 2. Features

- Input Voltage Range: 1.2V to 5.5V
- R<sub>DS(ON)</sub> =50mΩ(Typ.) @V<sub>IN</sub>=5.5V
- Built-in Slew Rate Control
- Ultra-low shutdown current
- 1.5A Maximum Continuous Switch Current
- 8kV ESD Rating
- Package: WLCSP4

#### 3. Applications

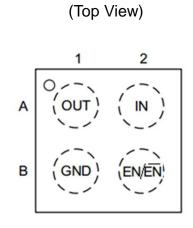
- Smartphones and Tablets
- Portable Devices
- Wearables

# 4. Typical Application





# **5. Pin Configuration**





# 6. Pin Description

PIN NUMBER	PIN NAME	I/O	PIN FUNCTION
A1	OUT	0	Switch output.
A2	IN	I	Switch input.
B1	GND		Common ground.
B2	EN/EN	I	Enable input. Active High for A/C Version, Active Low for B/D Version.

# 7. Production Model List

PART NUMBER	EN PIN ACTIVITY	QOD	RCB
WP2101-AC04R	Active High	Y	Y
WP2101-BC04R	Active Low	Y	Y
WP2101-CC04R	Active High	Y	N
WP2101-DC04R	Active Low	Y	Ν

# 8. Absolute Maximum Ratings <sup>[1]</sup>

Over operating free-air temperature range (unless otherwise noted)

PARAMETER	RATING	UNIT
IN, EN/EN Voltage	-0.3 to 6.0	V
OUT Voltage	-0.3 to 6.0	V
Maximum Continuous Switch Current for $V_{IN} \ge 2V^{[1]}$	1.5	A
Maximum Peak Switch Current for V <sub>IN</sub> ≥2V <sup>[2]</sup>	2	A
Junction-to-ambient Thermal Resistance( $\theta_{JA}$ )	193	°C/W
Junction-to-case(top) Thermal Resistance( $\theta_{Jc}$ )	2.3	°C/W
Junction Temperature	150	°C
Storage Temperature	-65 to 150	°C
Lead Temperature (Soldering, 10 sec)	260	°C
ESD(HBM)	8000	V

**Note1:** Limited by thermal design.

Note2: Limited by thermal design, and tested in 10ms width pulse current.

**Note3:** Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

# 9. Recommended Operating Conditions

SYMBOL	PARAMETER	MIN	МАХ	UNIT
V <sub>IN</sub>	Input Voltage	1.2	5.5	V
V <sub>EN</sub>	EN Input Voltage		5.5	V
Ιουτ	Output Current		1.5	А
T <sub>A</sub>	Operating Ambient Temperature	-40	85	°C
TJ	Operating Junction Temperature	-40	125	°C



### **10. Electrical Characteristics**

(V\_{IN} = 3.3 V, C\_{IN} = 1 \mu F, C\_{OUT} = 1 \mu F, T\_A = 25  $^\circ \mathrm{C}$  , unless otherwise noted)

SYMBOL	PARAMETER	TEST CONDITIONS		MIN	ТҮР	MAX	UNIT
		Disabled, V <sub>IN</sub> =1.2V,			3	18	
		OUT floating, T <sub>A</sub> =25°C					-
		Disabled, $V_{IN}$ =1.8V, OUT floating, T <sub>A</sub> =25°C			5	20	
	Shutdown Quiescent		Disabled, $V_{IN}$ =3.3V,				
I <sub>SHDN</sub>	Current		OUT floating, $T_A=25^{\circ}C$		8	45	nA
			Disabled, V <sub>IN</sub> =4.5V,		10	65	-
			UT floating, T <sub>A</sub> =25°C				
			Disabled, V <sub>IN</sub> =5.5V, UT floating, T <sub>A</sub> =25°C		20	95	
			Enabled, V <sub>IN</sub> =3.3V,		000		
		А	OUT floating, T <sub>A</sub> =25°C		230		_
	Quiescent Current	~~~~	Enabled, V <sub>IN</sub> =5.5V,		410		
			OUT floating, $T_A=25^{\circ}C$				-
		В	Enabled, $V_{IN}$ =3.3V, OUT floating, T <sub>A</sub> =25°C		550		
l <sub>Q</sub>			Enabled, V <sub>IN</sub> =5.5V,				nA
			OUT floating, T <sub>A</sub> =25°C		1000		
		C/D	Enabled, V <sub>IN</sub> =3.3V,		2	70	
			OUT floating, T <sub>A</sub> =25°C				
			Enabled, $V_{IN}$ =5.5V, OUT floating, T <sub>A</sub> =25°C		5	90	
		V <sub>IN</sub> =1.2V, I <sub>OUT</sub> =0.2A			225	260	
5	Switch On-resistance	V <sub>IN</sub> =1.8V, I <sub>OUT</sub> =0.2A			100	140	
$R_{DS(ON)}$		V <sub>IN</sub> =3.3V, I <sub>OUT</sub> =0.2A			55	90	mΩ
			V <sub>IN</sub> =5.5V, I <sub>OUT</sub> =0.2A		50	70	
I <sub>SINK</sub>	EN Input leakage	$V_{IN} = 0V \& V_{EN} = 5V$ for A/C; $V_{IN} = 0V \& V_{EN} = 0V$ for B/D		-1000		1000	nA
V <sub>IH</sub>	EN Input Logic High Voltage			1.2			V
VIL	EN Input Logic Low Voltage					0.4	V
R <sub>EN</sub>	EN Pin Resistor				12.5		MΩ

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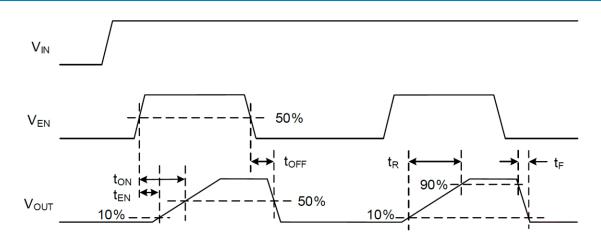
# WP2101

#### Ultra-Small Load Switch with Slew Rate Control

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	ТҮР	МАХ	UNIT		
R <sub>DIS</sub>	OUT Discharge Resistor	Disabled, V <sub>IN</sub> =3.3V, I <sub>SINK</sub> =2mA		90		Ω		
t <sub>R</sub>	OUT Rise Time	V <sub>IN</sub> =3.3V, C <sub>OUT</sub> =1uF, R <sub>OUT</sub> =30Ω		900		us		
t <sub>F</sub>	OUT Fall Time	V <sub>IN</sub> =3.3V, C <sub>OUT</sub> =1uF, R <sub>OUT</sub> =30Ω		110		us		
t <sub>ON</sub>	OUT Turn-on Time	V <sub>IN</sub> =3.3V, C <sub>OUT</sub> =1uF, R <sub>OUT</sub> =30Ω		1100		us		
toff	OUT Turn-off Time	V <sub>IN</sub> =3.3V, C <sub>OUT</sub> =1uF, R <sub>OUT</sub> =30Ω		70		us		
t <sub>EN</sub>	Enable Time	V <sub>IN</sub> =3.3V, C <sub>OUT</sub> =1uF, R <sub>OUT</sub> =30Ω		700		us		
Reverse Cur	Reverse Current Block (Only for WP2101-AC04R, WP2101-BC04R)							
V <sub>REV</sub>	Reverse Current Voltage Threshold	V <sub>IN</sub> =3.3V, C <sub>OUT</sub> =1uF,		60		mV		
$V_{\text{REV}_{\text{HYS}}}$	Reverse Current Voltage Hysteresis	V <sub>IN</sub> =3.3V, C <sub>OUT</sub> =1uF,		36		mV		
I <sub>REV-ACT</sub> <sup>[5]</sup>	Reverse Activation Current	V <sub>IN</sub> =3.3V, C <sub>OUT</sub> =1uF, V <sub>OUT</sub> > V <sub>IN</sub>		0.8		A		
I <sub>RCB</sub>	Reverse Block Leakage	$V_{OUT}$ - $V_{IN}$ > $V_{REV}$		0.2		μA		

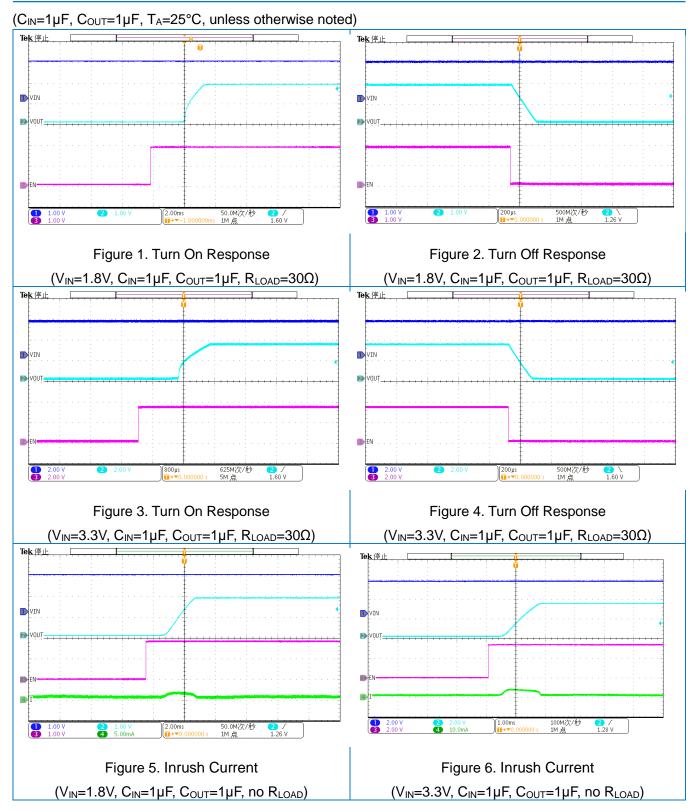
**Note4:** Limits over full temperature are guaranteed by design, but not tested in production. **Note5:** Guaranteed by design.

# **11. Timing Diagram**





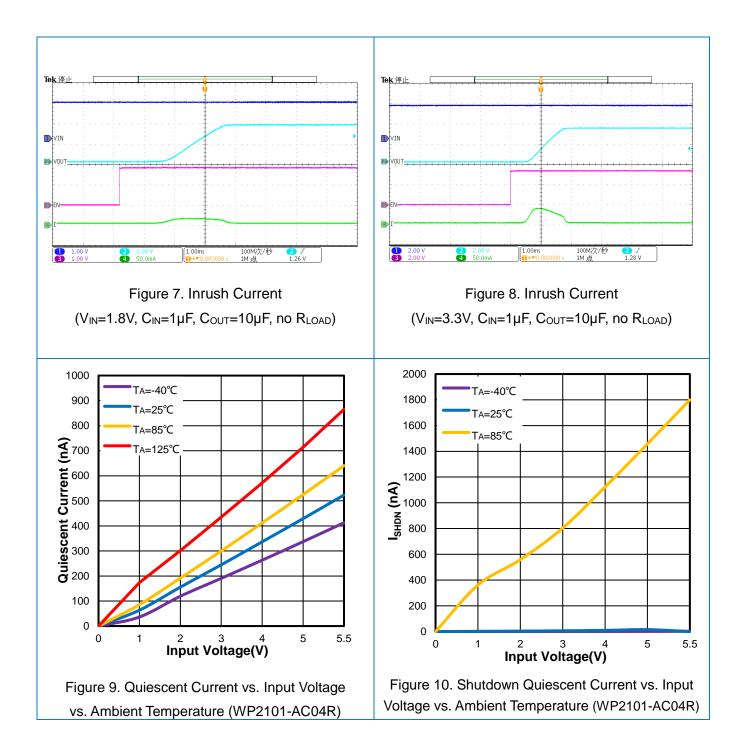
# **12. Typical Performance Characteristics**





# WP2101

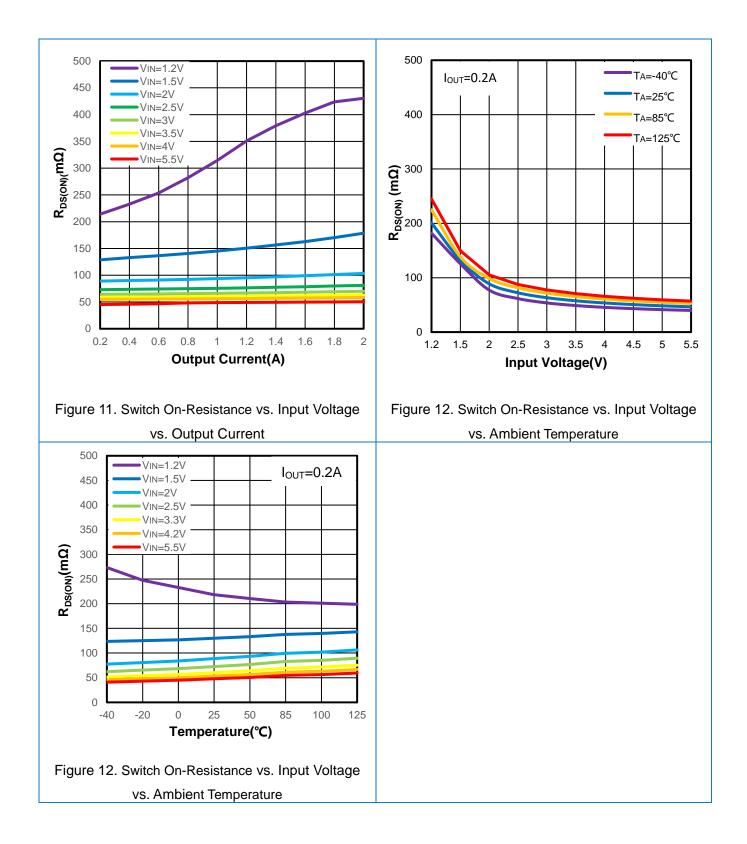
#### Ultra-Small Load Switch with Slew Rate Control



# <u>WAYØN</u>

# WP2101

#### Ultra-Small Load Switch with Slew Rate Control

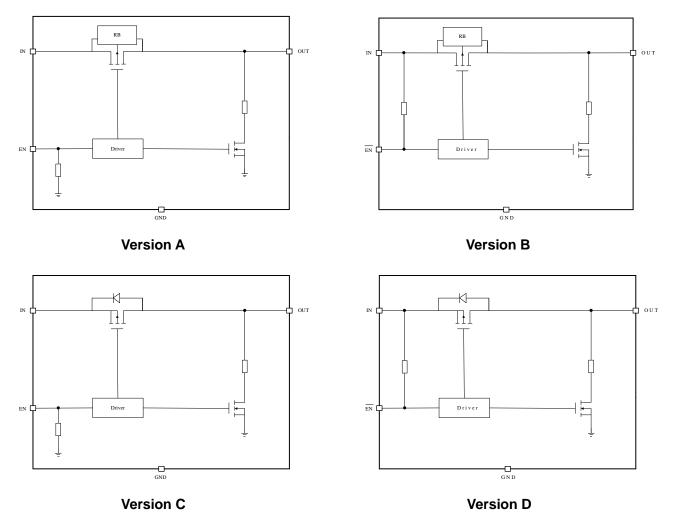


### **13. Function Description**

#### 13.1 Overview

The WP2101 series are 5.5V load switches in a WLCSP4 package. The devices contain a 50 m $\Omega$  currentlimited P-channel MOSFET that can operate over an input voltage range of 1.2 V to 5.5V. The WP2101 series has 4 versions (A/B/C/D) according with EN high active or low active and reverse block function is or not.

#### 13.2 Block Diagram



#### **13.3 Feature Description**

#### 13.3.1 Quick Output Discharge

The WP2101 include the Quick Output Discharge (QOD) feature, in order to discharge the application capacitor connected on OUT pin.

#### 13.3.2 Slew Rate Control

When the switch is enabled, the device regulates the gate voltage of MOSFET and controls the VOUT slew rate during  $t_R$  to avoid a large input inrush current. The feature reduces the interference to the power supply.

#### 13.3.3 EN Control

EN is high active for A and C version, and low active for B and D version. The device integrates  $12.5M\Omega$  pull down resistor for high active version and  $12.5M\Omega$  pull up resistor for low active version.

#### 13.3.4 Reverse Current Block (Only for WP2101-AC04R, WP2101-BC04R)

There is a Reverse Current Block function for WP2101-AC04R and WP2101-BC04R when  $V_{OUT}$  is 60mV greater than  $V_{IN}$ , which can prevent the current to flowing through the P-MOSFET or the body diode.

#### **13.4 Device Functional Modes**

When the EN pin is actively pulled high and no fault conditions are present, the switch will be turned on, connecting  $V_{IN}$  to  $V_{OUT}$ . When the EN pin is disabled regardless of the fault condition, the switch will be turned off.

#### **14. Application and Implementation**

#### 14.1 Application Information

#### 14.1.1 EN Control

The EN pin controls the state of the switch. Activating EN continuously holds the switch in the on state as long as there is no fault. An undervoltage lockout or thermal shutdown event will override the EN pin control and turn off the switch. EN is active high and has a low threshold, making it capable of interfacing with low-voltage signals.

#### 14.1.2 Input Capacitor

To limit the voltage drop on the input supply caused by transient inrush current, a capacitor 1µF or larger must be placed between the IN and GND pins.

#### 14.1.3 Output Capacitor

A 1µF or larger capacitor should be placed between the OUT and GND pins. This capacitor will prevent parasitic board inductances from forcing OUT below GND when the switch turns off.

#### 14.1.4 Power Dissipation and Junction Temperature

The junction temperature of the switch depend on several factors such as the load, PCB layout, ambient temperature and package type. Power dissipation can be calculated based on the output current and the  $R_{DS(ON)}$  of the switch as below.

$$P_D = R_{DS(ON)} \times I^2$$

The junction temperature can be estimated by the following thermal equation:

$$T_{\rm J} = P_{\rm D} \ x \ \theta_{\rm JA} + T_{\rm A}$$

Where:

T<sub>A</sub> = Ambient temperature



 $\theta_{JA}$  = Thermal resistance P<sub>D</sub> = Total power dissipation

With all possible conditions, the junction temperature must be within the range specified under operating conditions. The maximum output current must be derated at higher ambient temperature to ensure the junction temperature does not exceed the maximum junction temperature which is  $125^{\circ}$ C.

### **15. Power Supply Recommendations**

The device is designed to operate from a  $V_{IN}$  range of 1.2V to 5.5 V. This supply must be well regulated and placed as close to the device terminal as possible with the recommended 1µF bypass capacitor. If the supply is located more than a few inches from the device terminals, additional bulk capacitance may be required in addition to the ceramic bypass capacitors. If additional bulk capacitance is required, an electrolytic, tantalum, or ceramic capacitor of 10 µF may be sufficient

#### 16. Layout

For best performance, all traces should be as short as possible, the input and output capacitors should be placed close to the device to minimize the effects that parasitic trace inductances may have on normal and short-circuit operation. The  $V_{IN}$  terminal should be bypassed to ground with low ESR ceramic bypass capacitors. The typical recommended bypass capacitance is 1µF ceramic with X5R or X7R dielectric. This capacitor should be placed as close to the device terminals as possible. Using wide traces for  $V_{IN}$ ,  $V_{OUT}$ , and GND will help minimize parasitic electrical effects along with minimizing the case to ambient thermal impedance.

#### 16.1 Layout Example

WAY.ØN	LOAD SWITCH WLCSP4 EVM V1.1
	<u>ດ</u>
	R3

#### **17. Evaluation Modules**

Evaluation Modules (EVMs) are available to help evaluate the device performance. We have evaluation modules for different packages, you can contact us by phone or address at the end to get the evaluation module or schematic.

The module names are listed in the table below.

NAME	PACKAGE	EVALUATION MODULE
WP2101	WLCSP4	LOAD SWITCH WLCSP4 EVM V1.1



### **18. Naming Conventions**

#### WP AB CC-D EEE F

**WP:** WAYON Protection IC;

A: Product Category –2: Load Switch;

**B:** Maximum Output Current – 1: ≤2A;

CC: Serial number;

**D:** EN Enable (High/Low) – A/C: EN High Enable; B/D: EN Low Enable;

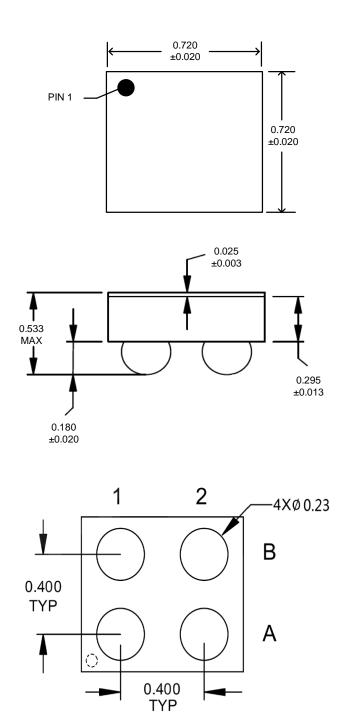
**EEE:** Package – C04: WLCSP4;

F: R-Reel & T-tube;



# **19. Package Information**

WLCSP4





# **20. Ordering Information**

PART NUMBER	OUTPUT CURRENT	PACKAGE	PACKING QUANTITY	MARKING*
WP2101-AC04R	1.5A	WLCSP4	3k/Reel	AXX
WP2101-BC04R	1.5A	WLCSP4	3k/Reel	BXX
WP2101-CC04R	1.5A	WLCSP4	3k/Reel	СХХ
WP2101-DC04R	1.5A	WLCSP4	3k/Reel	DXX

\* XX is variable.

#### **Contact Information**

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Specifications are subject to change without notice. The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time. Users should verify actual device performance in their specific applications.



WP21

#### **Product Specification Statement**

- The product specification aims to provide users with a reference regarding various product parameters, performance, and usage. It presents certain aspects of the product's performance in graphical form and is intended solely for users to select product and make product comparisons, enabling users to better understand and evaluate the characteristics and advantages of the product. It does not constitute any commitment, warranty, or guarantee.
- The product parameters described in the product specification are numerical values, characteristics, and functions obtained through actual testing or theoretical calculations of the product in an independent or ideal state. Due to the complexity of product applications and variations in test conditions and equipment, there may be slight fluctuations in parameter test values. WAYON shall not guarantee that the actual performance of the product when installed in the customer's system or equipment will be entirely consistent with the product specification, especially concerning dynamic parameters. It is recommended that users consult with professionals for product selection and system design. Users should also thoroughly validate and assess whether the actual parameters and performance when installed in their respective systems or equipment meet their requirements or expectations. Additionally, users should exercise caution in verifying product compatibility issues, and WAYON assumes no responsibility for the application of the product.
- WAYON strives to provide accurate and up-to-date information to the best of our ability. However, due to technical, human, or other reasons, WAYON cannot guarantee that the information provided in the product specification is entirely accurate and error-free. WAYON shall not be held responsible for any losses or damages resulting from the use or reliance on any information in these product specifications. WAYON reserves the right to revise or update the product specification and the products at any time without prior notice, and the user's continued use of the product specification is considered an acceptance of these revisions and updates. Prior to purchasing and using the product, users should verify the above information with WAYON to ensure that the product specification is the most current, effective, and complete. If users are particularly concerned about product parameters, please consult WAYON in detail or request relevant product test reports. Any data not explicitly mentioned in the product specification shall be subject to separate agreement.
- Users are advised to pay attention to the parameter limit values specified in the product specification and maintain a certain margin in design or application to ensure that the product does not exceed the parameter limit values defined in the product specification. This precaution should be taken to avoid exceeding one or more of the limit values, which may result in permanent irreversible damage to the product, ultimately affecting the quality and reliability of the system or equipment.
- The design of the product is intended to meet civilian needs and is not guaranteed for use in harsh environments or precision equipment. It is not recommended for use in systems or equipment such as medical devices, aircraft, nuclear power, and similar systems, where failures in these systems or equipment could reasonably be expected to result in personal injury. WAYON shall assume no responsibility for any consequences resulting from such usage.
- Users should also comply with relevant laws, regulations, policies, and standards when using the product specification. Users are responsible for the risks and liabilities arising from the use of the product specification and must ensure that it is not used for illegal purposes. Additionally, users should respect the intellectual property rights related to the product specification and refrain from infringing upon any third-party legal rights. WAYON shall assume no responsibility for any disputes or controversies arising from the above-mentioned issues in any form.