

30V P-Channel Enhancement Mode Power MOSFET

Description

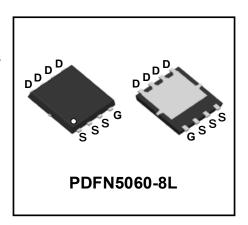
WMB60P03TA uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

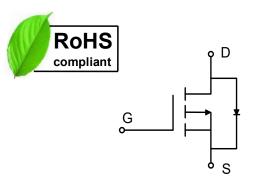
Features

- V_{DS} = -30V, I_{D} = -60A Typ. $R_{DS(on)}$ = 5.3m Ω @ V_{GS} = -10V Typ. $R_{DS(on)}$ = 8.0m Ω @ V_{GS} = -4.5V
- Green Device Available
- Low Gate Charge
- 100% EAS Guaranteed
- RoHS Compliant & Halogen-Free

Applications

- Load Switch
- Power Management Switches





Absolute Maximum Ratings (T_A = 25°C, unless otherwise noted)

Parameter		Symbol	Value	Unit	
Drain-Source Voltage		V _{DS}	-30	V	
Gate-Source Voltage		V _{GS}	±20	V	
Ocation and David Ocament	T _C =25°C		-60	А	
Continuous Drain Current	T _C =100°C	lο	-38		
Pulsed Drain Current ¹		I _{DM}	-240	А	
Single Pulse Avalanche Energy²		EAS	61.25	mJ	
Total Power Dissipation	T _C =25°C	P _D	48	W	
Operating Junction and Storage Temperature Range		TJ, T _{STG}	-55 to150	°C	

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ³	Reja	42	°C/W
Thermal Resistance from Junction-to-Case	Rejc	2.6	°C/W

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Electrical Characteristics (T_J = 25°C, unless otherwise noted)

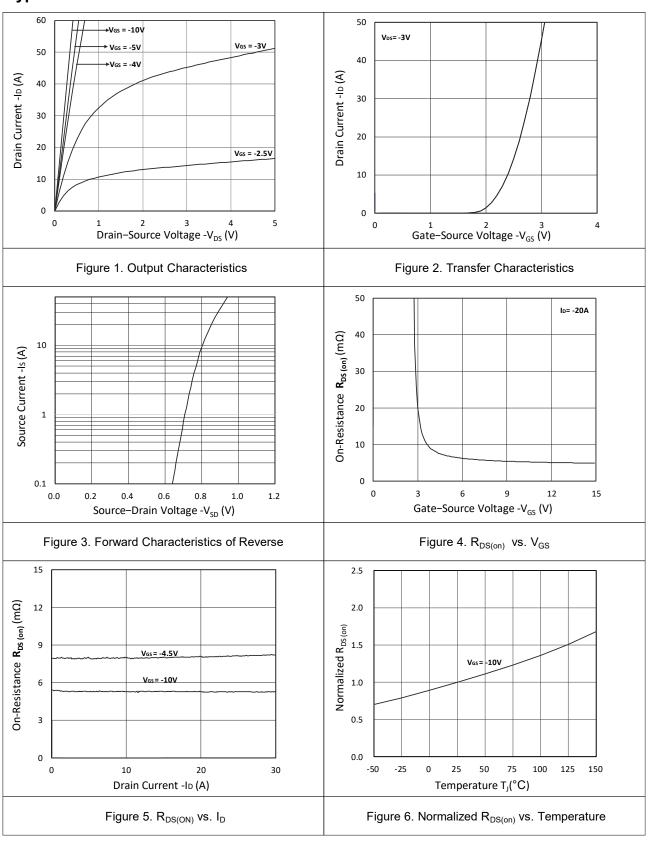
Parameter		Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static Characteristics								
Drain-Source Breakdown Vo	e Breakdown Voltage V _{(BR)DSS} V _{GS} = 0V, I _D = -250µA		V _{GS} = 0V, I _D = -250μA	-30	-	-	V	
Gate-body Leakage current		Igss	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA	
Zero Gate Voltage Drain	TJ=25°C		201/1/	-	-	-1	μA	
Current	T _J =100°C	IDSS	$V_{DS} = -30V$, $V_{GS} = 0V$	-	-	-100		
Gate-Threshold Voltage		V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1	-1.5	-2	V	
Drain-Source On-Resistance	.4	_	V _{GS} = -10V, I _D = -20A	-	5.3	6.8		
Drain-Source On-Resistance	,,	R _{DS(on)}	V _{GS} = -4.5V, I _D = -10A	-	8	10.5	mΩ	
Forward Transconductance ⁴		g fs	V _{DS} = -10V, I _D = -20A	-	50	-	S	
Dynamic Characteristics	5 ⁵							
Input Capacitance		C _{iss}		-	3220	-		
Output Capacitance		Coss	$V_{DS} = -15V, V_{GS} = 0V,$ f = 1MHz	-	465	-	pF	
Reverse Transfer Capacitan	ce	Crss		-	405	-		
Gate Resistance	Gate Resistance		f=1MHz	-	9.5	-	Ω	
Switching Characteristic	cs ⁵							
Total Gate Charge		Qg		-	70	-		
Gate-Source Charge		Q _{gs}	$V_{GS} = -10V, V_{DS} = -15V,$ $I_{D} = -20A$	-	9.9	-	nC	
Gate-Drain Charge		\mathbf{Q}_{gd}		-	10.5	-		
Turn-On Delay Time		t _{d(on)}		-	10.8	-		
Rise Time		tr	$V_{GS} = -10V, V_{DD} = -15V,$ $R_{G} = 3\Omega, I_{D} = -20A$	-	13.2	-	ns ns	
Turn-Off Delay Time		t _{d(off)}		-	73	-		
Fall Time		t f		-	35	-		
Body Diode Reverse Recove	ery Time	t _{rr}	1 004 11/11 4004/		25		ns	
Body Diode Reverse Recovery Charge		Qrr	- I _F = -20A, di/dt = -100A/μs		10		nC	
Drain-Source Body Diode Characteristics								
Diode Forward Voltage ⁴		V _{SD}	I _S = -20A, V _{GS} = 0V	-	-	-1.2	V	
Continuous Source Current	Tc=25°C	ls	-	-	-	-60	Α	

Notes:

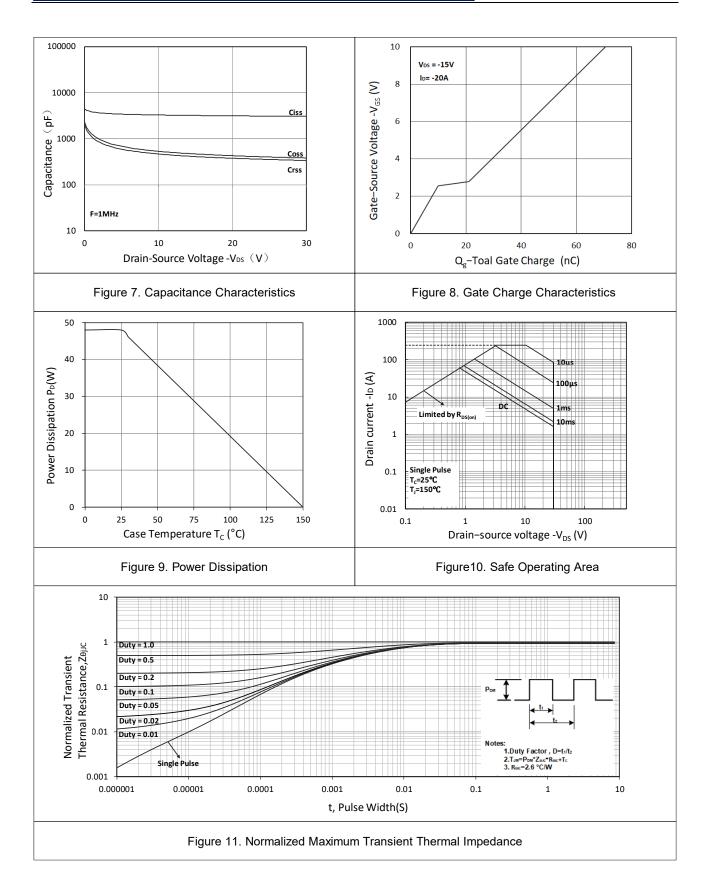
- 1. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$ =150°C.
- 2. The test condition is $V_{\text{DD}}\text{= -25V},\,V_{\text{GS}}\text{= -10V},\,L\text{=}0.1\text{mH},\,I_{\text{AS}}\text{= -35A}$
- 3. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
- 4. The data tested by pulsed, pulse width \leq 300us, duty cycle \leq 2%.
- 5. This value is guaranteed by design hence it is not included in the production test.



Typical Characteristics









Test Circuit

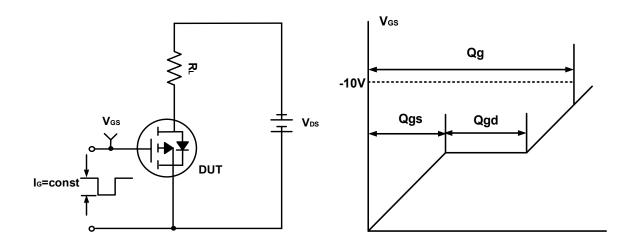


Figure A. Gate Charge Test Circuit & Waveforms

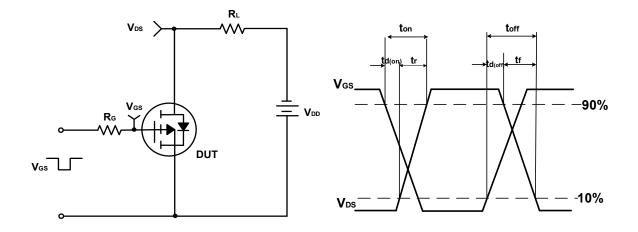


Figure B. Switching Test Circuit & Waveforms

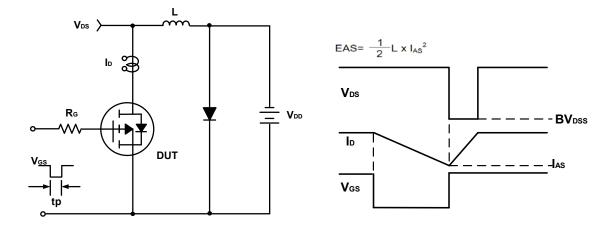
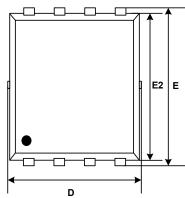
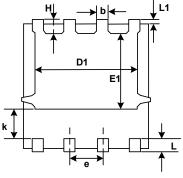


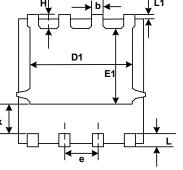
Figure C. Unclamped Inductive Switching Circuit & Waveforms



Mechanical Dimensions for PDFN5060-8L







COMMON DIMENSIONS

SYMBOL	MM		
STIVIDOL	MIN	MAX	
Α	0.90	1.20	
A3	0.15	0.35	
D	4.80	5.40	
E	5.90	6.35	
D1	3.61	4.31	
E1	3.30	3.92	
E2	5.50	6.06	
k	1.10	-	
b	0.30	0.51	
е	1.27	7BSC	
L	0.38	0.71	
L1	0.05	0.36	
Н	0.38	0.71	
θ	0°	12°	



Ordering Information

Part	Package	Marking	Packing method
WMB60P03TA	PDFN5060-8L	B60P03A	Tape and Reel

Marking Information



B60P03A= Device code XXXX XXX= Date code

Contact Information

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- 1. The product specification aims to provide users with a reference regarding various product parameters, performance, and usage. It presents certain aspects of the product's performance in graphical form and is intended solely for users to select product and make product comparisons, enabling users to better understand and evaluate the characteristics and advantages of the product. It does not constitute any commitment, warranty, or guarantee.
- 2. The product parameters described in the product specification are numerical values, characteristics, and functions obtained through actual testing or theoretical calculations of the product in an independent or ideal state. Due to the complexity of product applications and variations in test conditions and equipment, there may be slight fluctuations in parameter test values. WAYON shall not guarantee that the actual performance of the product when installed in the customer's system or equipment will be entirely consistent with the product specification, especially concerning dynamic parameters. It is recommended that users consult with professionals for product selection and system design. Users should also thoroughly validate and assess whether the actual parameters and performance when installed in their respective systems or equipment meet their requirements or expectations. Additionally, users should exercise caution in verifying product compatibility issues, and WAYON assumes no responsibility for the application of the product.
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