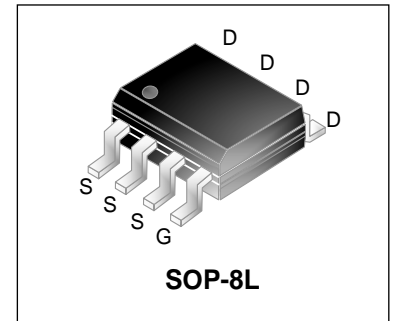


100V N-Channel Enhancement Mode Power MOSFET

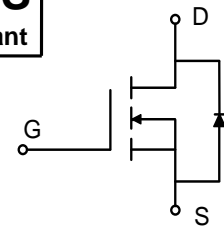
Description

WMS080N10LG4 uses Wayon's 4th generation power trench MOSFET technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance. This device is well suited for high efficiency fast switching applications.



Features

- $V_{DS} = 100V$, $I_D = 13A$
 $R_{DS(on)} < 9m\Omega @ V_{GS} = 10V$
 $R_{DS(on)} < 12m\Omega @ V_{GS} = 4.5V$
- Green Device Available
- RoHS Compliant & Halogen-Free
- 100% EAS Guaranteed
- Optimized for High Speed Smooth Switching



Applications

- Power Management Switches
- DC/DC Converters
- Synchronous Rectification

Absolute Maximum Ratings ($T_A = 25^\circ C$, unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_A = 25^\circ C$	I_D	13	A
	$T_A = 100^\circ C$		8.2	
Pulsed Drain Current ¹		I_{DM}	52	A
Single Pulse Avalanche Energy ²		EAS	135.2	mJ
Total Power Dissipation	$T_A = 25^\circ C$	P_D	3.1	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ³	$R_{\theta JA}$	40	$^\circ C/W$

Electrical Characteristics ($T_J = 25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	100	-	-	V	
Gate-Body Leakage Current	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	± 100	nA	
Zero Gate Voltage Drain Current	$T_J=25^\circ\text{C}$	I_{DSS}	$V_{DS} = 100V, V_{GS} = 0V$	-	-	1	μA
	$T_J=100^\circ\text{C}$			-	-	100	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.2	1.7	2.2	V	
Drain-Source on-Resistance ⁴	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 10A$	-	7	9	m Ω	
		$V_{GS} = 4.5V, I_D = 6A$	-	9.2	12		
Forward Transconductance ⁴	g_{fs}	$V_{DS} = 10V, I_D = 20A$	-	80	-	S	
Dynamic Characteristics⁵							
Input Capacitance	C_{iss}	$V_{DS} = 50V, V_{GS} = 0V,$ $f = 1\text{MHz}$	-	1860	-	μF	
Output Capacitance	C_{oss}		-	725	-		
Reverse Transfer Capacitance	C_{rss}		-	15.6	-		
Gate Resistance	R_G	$f = 1\text{MHz}$	-	2	-	Ω	
Switching Characteristics⁵							
Total Gate Charge	Q_g	$V_{GS} = 10V, V_{DS} = 50V,$ $I_D = 10A$	-	32.3	-	nC	
Gate-Source Charge	Q_{gs}		-	4.6	-		
Gate-Drain Charge	Q_{gd}		-	6.4	-		
Turn-on Delay Time	$t_{d(on)}$	$V_{GS} = 10V, V_{DD} = 50V,$ $R_G = 3\Omega, I_D = 10A$	-	9	-	ns	
Rise Time	t_r		-	6.1	-		
Turn-off Delay Time	$t_{d(off)}$		-	31	-		
Fall Time	t_f		-	13.8	-		
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 10A, di/dt = 100A/\mu s$	-	52	-	ns	
Body Diode Reverse Recovery Charge	Q_{rr}		-	57.2	-	nC	
Drain-Source Body Diode Characteristics							
Diode Forward Voltage ⁴	V_{SD}	$I_S = 10A, V_{GS} = 0V$	-	-	1.2	V	
Continuous Source Current	I_S	$T_A=25^\circ\text{C}$	-	-	13	A	

Notes:

1. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^\circ\text{C}$.
2. The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.4\text{mH}, I_{AS}=26A$.
3. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
5. This value is guaranteed by design hence it is not included in the production test.

Typical Characteristics

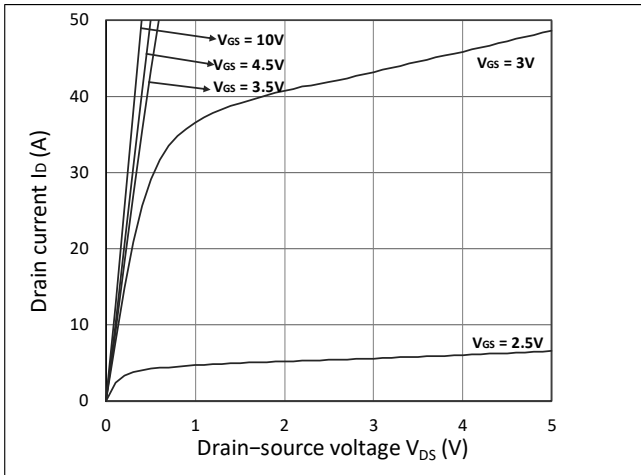


Figure 1. Output Characteristics

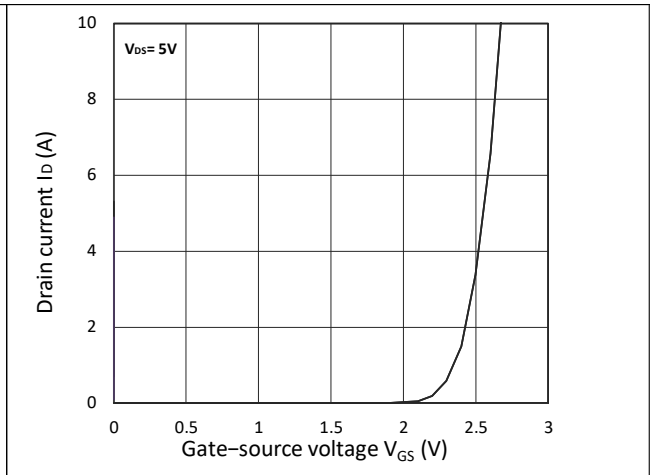


Figure 2. Transfer Characteristics

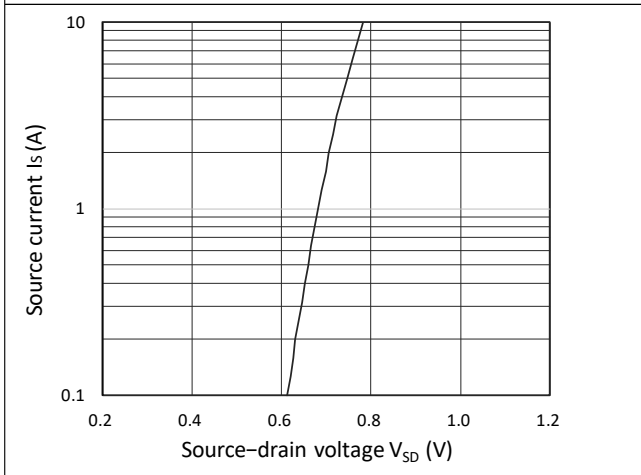


Figure 3. Forward Characteristics of Reverse

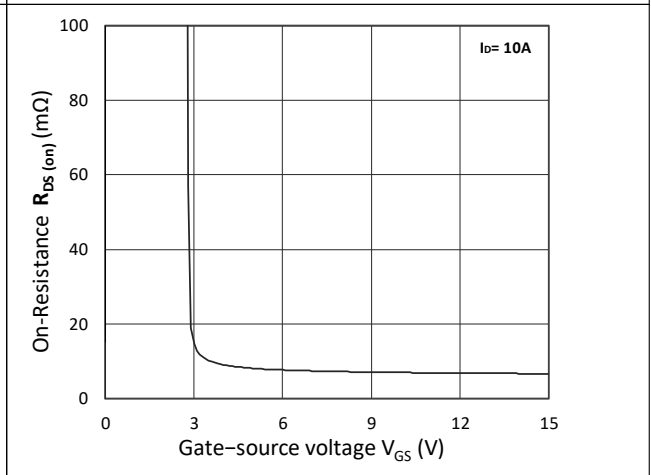


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

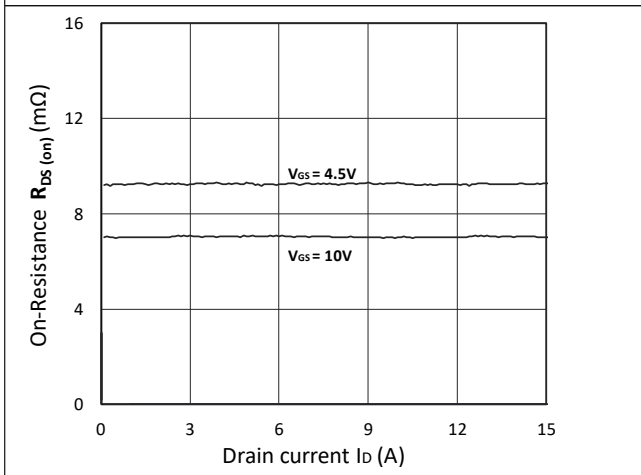


Figure 5. $R_{DS(ON)}$ vs. I_D

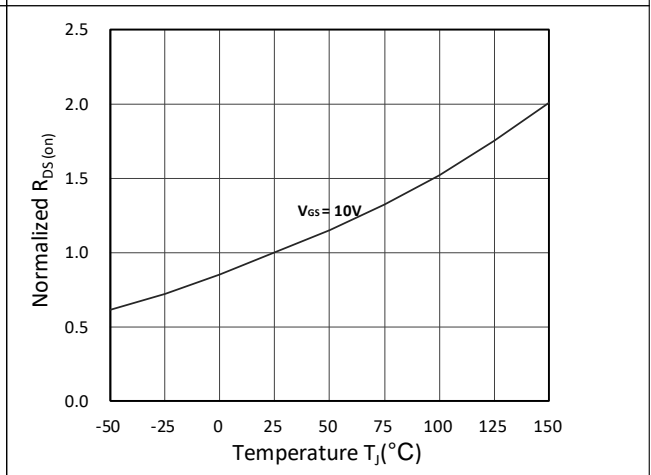


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

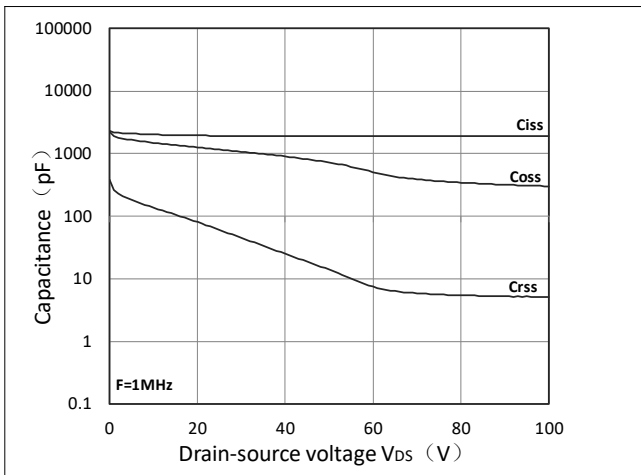


Figure 7. Capacitance Characteristics

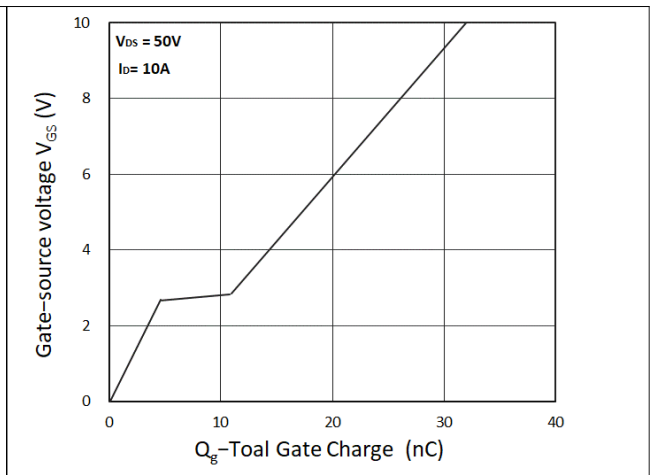


Figure 8. Gate Charge Characteristics

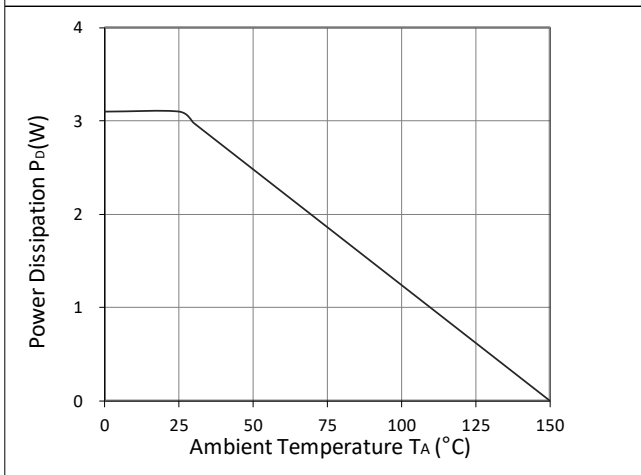


Figure 9. Power Dissipation

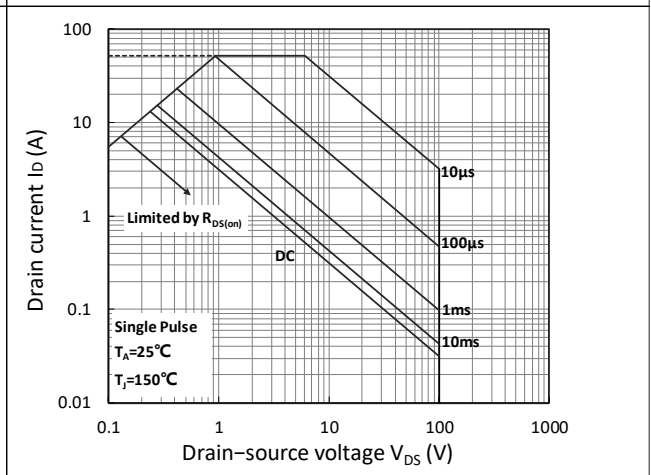


Figure 10. Safe Operating Area

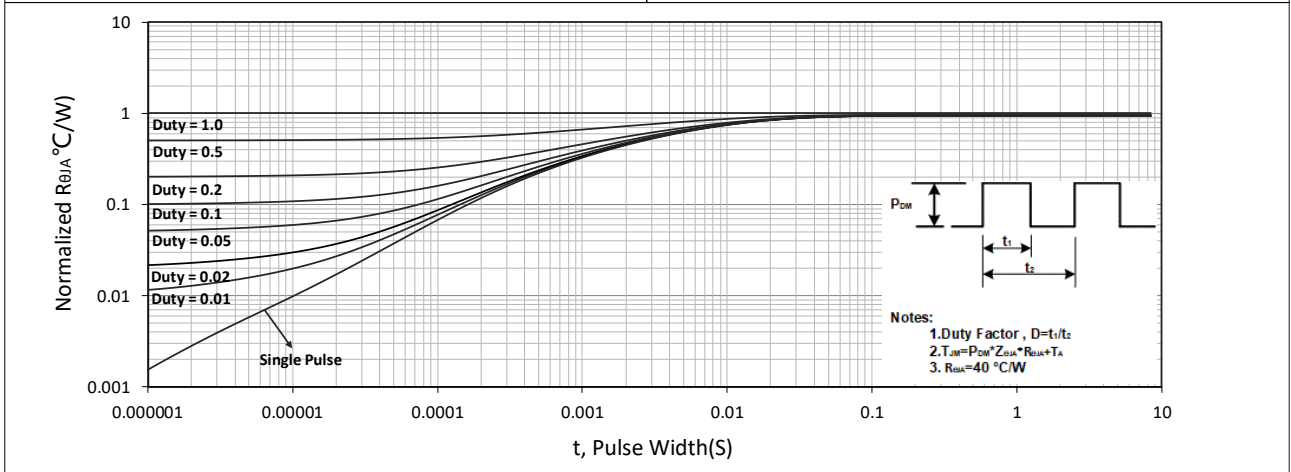


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

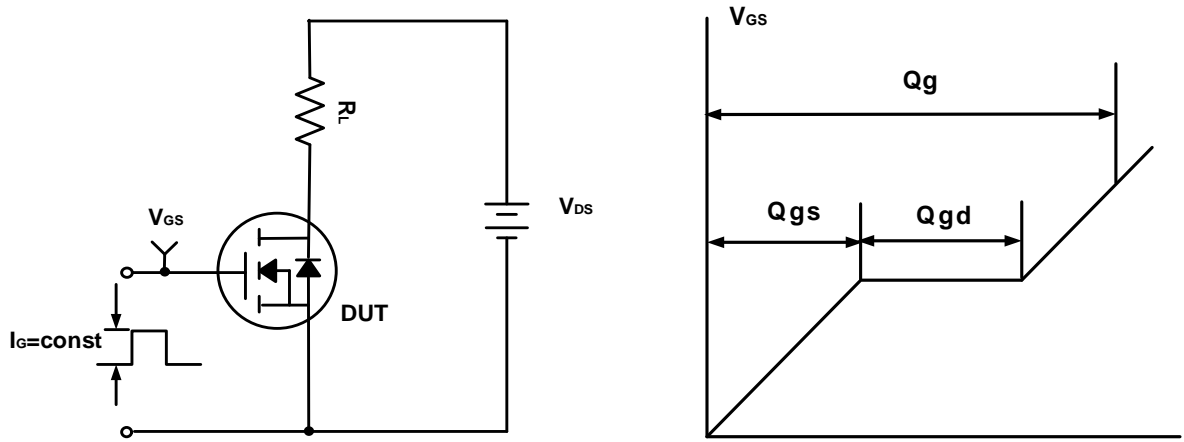


Figure A. Gate Charge Test Circuit & Waveforms

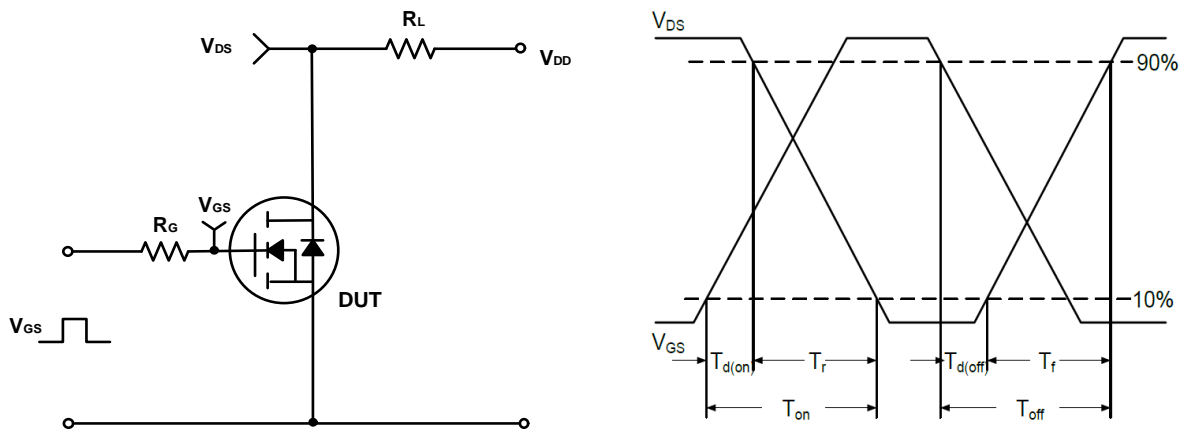
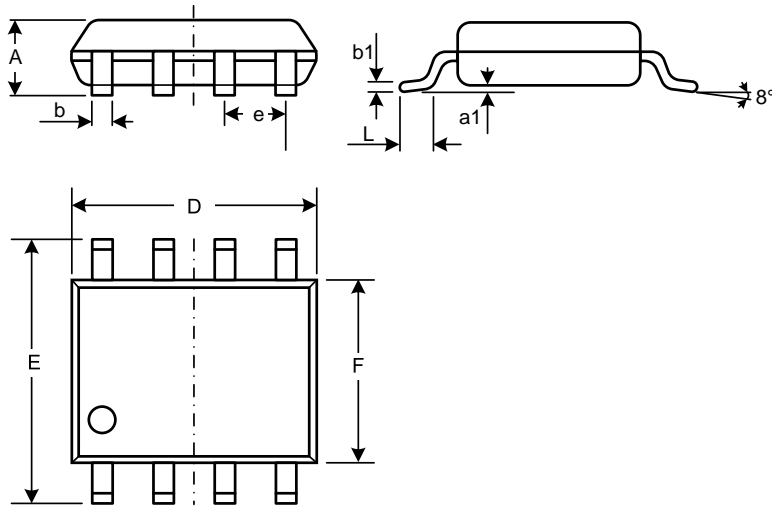


Figure B. Switching Test Circuit & Waveforms



Figure C. Unclamped Inductive Switching Circuit & Waveforms

Mechanical Dimensions for SOP-8L

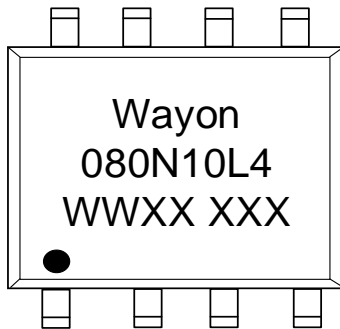


COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	1.35	1.75
a1	0.05	0.25
b	0.31	0.51
b1	0.16	0.25
D	4.70	5.15
E	5.75	6.25
e	1.07	1.47
F	3.70	4.10
L	0.40	1.27

Ordering Information

Part	Package	Marking	Packing method
WMS080N10LG4	SOP-8L	080N10L4	Tape and Reel

Marking Information

080N10L4 = Device code

WWXX XXX= Date code

Contact Information

No.1001, Shiwan(7) Road, Pudong District, Shanghai, P.R.China.201207

Tel: 86-21-50310888 Fax: 86-21-50757680 Email: market@way-on.com

WAYON website: <http://www.way-on.com>

For additional information, please contact your local Sales Representative.

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2. The product parameters described in the product specification are numerical values, characteristics, and functions obtained through actual testing or theoretical calculations of the product in an independent or ideal state. Due to the complexity of product applications and variations in test conditions and equipment, there may be slight fluctuations in parameter test values. WAYON shall not guarantee that the actual performance of the product when installed in the customer's system or equipment will be entirely consistent with the product specification, especially concerning dynamic parameters. It is recommended that users consult with professionals for product selection and system design. Users should also thoroughly validate and assess whether the actual parameters and performance when installed in their respective systems or equipment meet their requirements or expectations. Additionally, users should exercise caution in verifying product compatibility issues, and WAYON assumes no responsibility for the application of the product.
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