

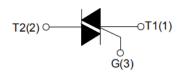
BTA06/BTB06 Series

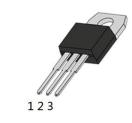
Silicon Controlled Rectifier

Features

- Blocking Voltage to 800 V
- Glass Passivated Surface for Reliability and Uniformity
- RoHS Compliant
- High Dv/Dt Rate
- IT(RMS) to 6A of Triacs

Pin Configuration







TO-220A(Ins)

TO-220B(No-Ins)





TO-220F(Ins)

TO-252-4R

Absolute Maximum Ratings (Tc=25°C Unless otherwise specified)

| Parameter | Symbol | Value | Unit |
|---|---------------------|---------|------------------|
| Storage junction temperature range | Tstg | -40~150 | $^{\circ}$ |
| Operating junction temperature range | Ti | -40~125 | $^{\circ}$ |
| Repetitive peak off-state voltage (Tj=25℃) | Vdrm | 800 | V |
| Repetitive peak reverse voltage (Tj=25°C) | Vrrm | 800 | V |
| RMS on-state current | İ T(RMS) | 6 | А |
| Non repetitive surge peak on-state current (full cycle, F=50Hz) | Ітѕм | 60 | А |
| I ² t value for fusing (tp=10ms) | I ² t | 18 | A ² s |
| Critical rate of rise of on-state current (IG=2×IGT) | dl/dt | 50 | A/µs |
| Peak gate current | Ідм | 2 | А |
| Average gate power dissipation | P _G (AV) | 1 | W |
| Peak gate power | Рсм | 5 | W |

SCR

| Thermal Resistance(between Junction and Case) @TO-220A(Ins) | R _θ (J-C) | 3.4 (Typ.) | °C/W |
|---|----------------------|------------|------|
| Thermal Resistance(between Junction and Case) @TO-220B(Non-Ins) | R _θ (J-C) | 2.2 (Typ.) | °C/W |
| Thermal Resistance(between Junction and Case) @TO-220F(Ins) | R _θ (J-C) | 3.2 (Typ.) | °C/W |
| Thermal Resistance(between Junction and Case) @TO-252-4R | R _{θ(J-C)} | 2.8 (Typ.) | °C/W |

Electronics Characteristics (Tc=25°C Unless otherwise specified)

3 Quadrants:

| Parameter | Sumbol | Quadrant | | Value | | | Unit | |
|--|--------------|------------------|-------|-------|-----|-----|------|------|
| Parameter | Symbol | Quadrant | | TW | SW | CW | BW | |
| Gate Trigger Current (Continuous dc) @VD=12V, RL=33Ω | Ідт | I - II -III | MAX | 5 | 10 | 35 | 50 | mA |
| Gate Trigger Voltage (Continuous dc) @VD=12V, RL=33Ω | Vgт | I II III W. U. | | | | 1.5 | | V |
| Gate non-trigger voltage@VD=VDRM | VGD | I - II -III | MIN | 0.2 | | V | | |
| Holding Current@IT=100mA | Ін | - | MAX | 6 | 10 | 35 | 60 | mA |
| Lotabing Current@IC 4 2ICT | | I -III | MAX | 10 | 15 | 50 | 70 | mA |
| Latching Current@IG=1.2IGT | ĮL. | II | IVIAA | 15 | 25 | 60 | 80 | IIIA |
| Critical Rate-of-Rise of Off State Voltage @VD=0.66×VDRM, Tj=125°C, Gate Open | dV/dt | - | MIN | 50 | 100 | 500 | 1000 | V/µs |
| Peak Forward On-State Voltage @ITM=8.5A,tp=380µs, Tj=25℃ | Vтм | - | MAX | 1.5 | | V | | |
| Peak Repetitive Forward @VDRM=VRRM,Tj=25℃ | DRM | - | MAX | | | 5 | | μА |
| Reverse Blocking Current @VDRM=VRRM,Tj=125℃ | İrrm | - | MAX | | | 0.5 | | mA |

Electronics Characteristics (Tc=25°C Unless otherwise specified)

4 Quadrants:

| Davarratas | Symbol | Quadrant | | Value | l lmit |
|--|--------------|-------------|-----|-------|--------|
| Parameter | | Quadrant | | С | - Unit |
| | | I - II -III | | 25 | mA |
| Gate Trigger Current (Continuous dc) @VD=12V, RL=33Ω | lgт | IV | MAX | 50 | mA |
| Gate Trigger Voltage (Continuous dc) @VD=12V, RL=33Ω | Vgт | ALL | | 1.5 | V |
| Gate non-trigger voltage@VD=VDRM | Vgd | ALL | MIN | 0.2 | V |
| Holding Current@IT=100mA | Ін | - | MAX | 40 | mA |
| Latching Current@IG=1.2IGT | I -III-IV | I -III-IV | MAX | 50 | mA |
| | | Will DX | 70 | mA | |
| Critical Rate-of-Rise of Off State Voltage @VD=0.66×VDRM, Tj=125℃,Gate Open | dV/dt | - | MIN | 200 | V/µs |
| Peak Forward On-State Voltage@ITM=8.5A,tp=380μs, Tj=25°C | Vтм | - | MAX | 1.5 | V |
| Peak Repetitive Forward@VDRM=VRRM,Tj=25℃ | I DRM | - | MAX | 5 | μА |
| Reverse Blocking Current@VDRM=VRRM,Tj=125℃ | RRM | - | MAX | 0.5 | mA |

Note: The above typical parameters or typical characteristics are only indicative and do not make specific guarantees. If detailed values are required, additional communication and provision are required.

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FIG.1: Maximum power dissipation versus RMS on-state current

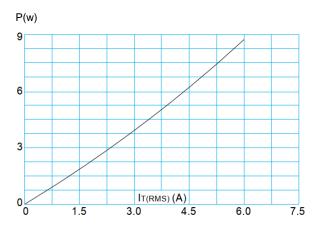


FIG.3: Surge peak on-state current versus number of cycles

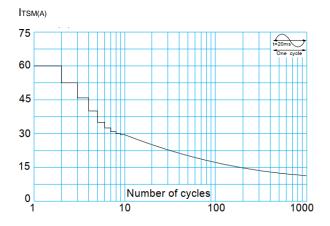


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<20ms, and corresponging value of I^2 t ($I - II - III : dI/dt < 50A/\mu s; IV : dI/dt < 10A/\mu s)$

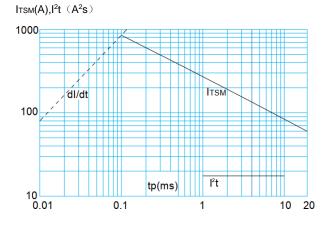


FIG.2: RMS on-state current versus case temperature in different packaging

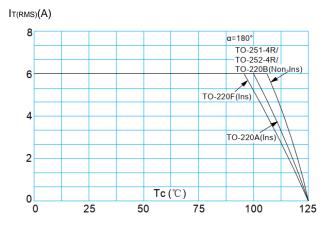


FIG.4:On-state characteristics (maximum values)

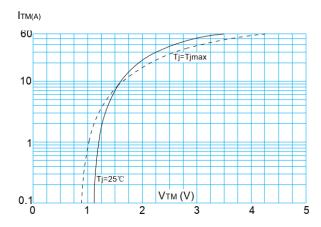
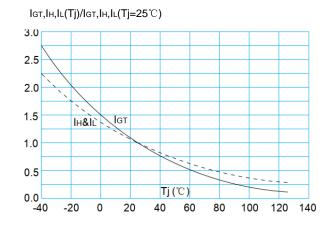
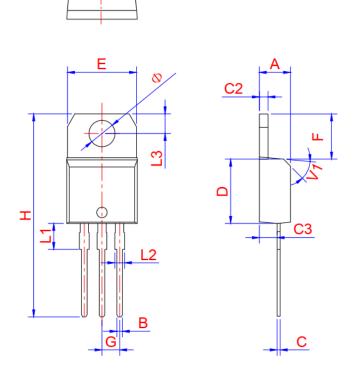


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature



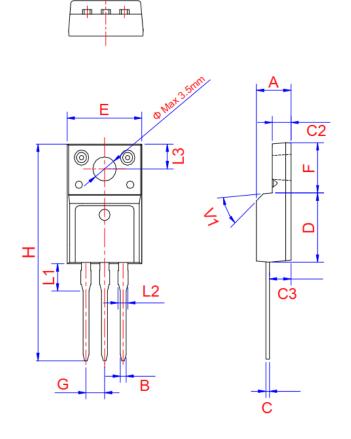
Outline Drawing- TO-220A Ins Or TO-220B Non-Ins

| <u> </u> | <u> </u> | | | |
|----------|----------|------|-------|--|
| SYMBOL | MM | | | |
| SYMBOL | MIN | NOM | MAX | |
| А | 4.20 | 4.47 | 4.60 | |
| В | 0.61 | - | 0.93 | |
| С | 0.41 | 0.50 | 0.70 | |
| C2 | 1.20 | 1.27 | 1.42 | |
| C3 | 2.40 | - | 2.72 | |
| D | 8.60 | - | 9.70 | |
| Е | 9.70 | - | 10.60 | |
| F | 6.15 | - | 7.15 | |
| G | - | 2.54 | - | |
| Н | 28 | - | 29.8 | |
| L1 | - | 3.75 | - | |
| L2 | 1.10 | - | 1.70 | |
| L3 | 2.55 | - | 2.95 | |
| V1 | - | 45° | - | |
| Ф | 3.65 | 3.75 | 3.85 | |



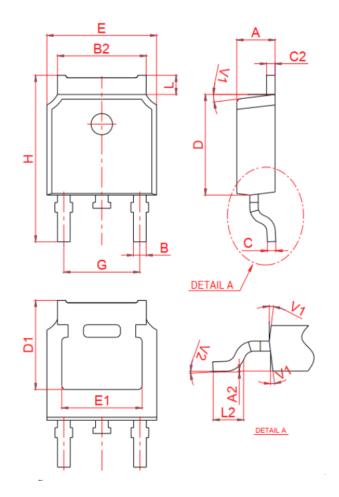
Outline Drawing- TO-220F Ins

| | - 1 a 11 1 g | | |
|--------|--------------|------|------|
| SYMBOL | MM | | |
| SYMBOL | MIN | NOM | MAX |
| Α | 4.50 | - | 4.90 |
| В | 0.58 | 0.8 | 0.90 |
| С | 0.40 | - | 0.65 |
| C2 | 2.34 | - | 2.75 |
| C3 | 2.56 | - | 3.00 |
| D | 8.80 | - | 9.30 |
| Е | 9.80 | - | 10.5 |
| F | 6.40 | - | 6.80 |
| G | - | 2.54 | - |
| Н | 28 | - | 29.8 |
| L1 | - | 3.63 | - |
| L2 | 1.14 | - | 1.70 |
| L3 | 2.65 | 3.30 | 3.85 |
| V1 | - | 45° | - |



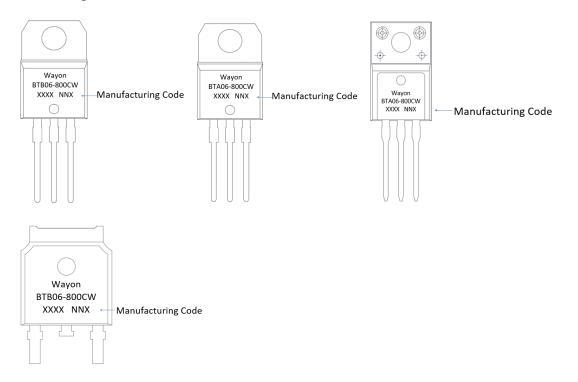
Outline Drawing- TO-252-4R

| CVANDOL | MM | | | |
|---------|---------|-----|-------|--|
| SYMBOL | MIN | NOM | MAX | |
| А | 2.10 | - | 2.50 | |
| A2 | 0 | - | 0.20 | |
| В | 0.66 | - | 0.91 | |
| B2 | 5.10 | - | 5.50 | |
| С | 0.46 | - | 0.58 | |
| C2 | 0.43 | - | 0.61 | |
| D | 5.90 | - | 6.30 | |
| D1 | 5.30REF | | | |
| E | 6.40 | - | 6.80 | |
| E1 | 4.63 | - | - | |
| G | 4.372 | - | 4.772 | |
| Н | 9.40 | - | 10.50 | |
| L | 0.88 | - | 1.28 | |
| L2 | 1.35 | - | 1.75 | |
| V1 | - | 7° | - | |
| V2 | 0° | - | 8° | |



Marking Code:

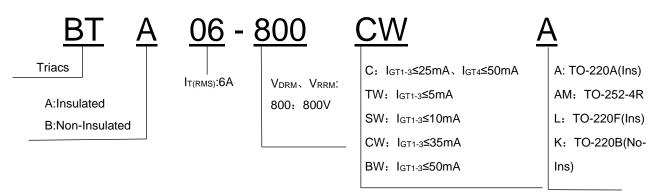
For Example:



TO-220B(No-Ins) ,TO-220A(Ins),TO-220F(Ins),TO-252-4R

Note: The second line of printed content is the result of removing the package code from the part number system

Part Number System



Package Information

| Package | Base qty. | Delivery mode |
|-----------------|-----------|---------------|
| TO-220A(Ins) | 50 | Tube |
| TO-220B(No-Ins) | 50 | Tube |
| TO-220F(Ins) | 50 | Tube |
| TO-252-4R | 2500 | Reel |

Contact Information

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WAYON website: http://www.way-on.com

For additional information, please contact your local Sales Representative.

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Product Specification Statement

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The product parameters described in the product specification are numerical values, characteristics, and functions obtained through actual testing or theoretical calculations of the product in an independent or ideal state. Due to the complexity of product applications and variations in test conditions and equipment, there may be slight fluctuations in parameter test values. WAYON shall not guarantee that the actual performance of the product when installed in the customer's system or equipment will be entirely consistent with the product specification, especially concerning dynamic parameters. It is recommended that users consult with professionals for product selection and system design. Users should also thoroughly validate and assess whether the actual parameters and performance when installed in their respective systems or equipment meet their requirements or expectations. Additionally, users should exercise caution in verifying product compatibility issues, and WAYON assumes no responsibility for the application of the product.

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