

60V P-Channel Enhancement Mode Power MOSFET

Description

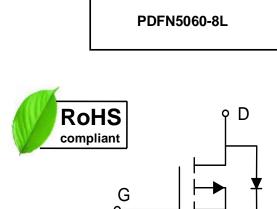
WMB65P06TS uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Features

- $V_{DS} = -60V$, $I_{D} = -60A$ $R_{DS(on)} < 15.5m\Omega$ @ $V_{GS} = -10V$ $R_{DS(on)} < 20m\Omega$ @ $V_{GS} = -4.5V$
- Excellent Stability and Uniformit
- RoHS Compliant & Halogen-Free
- Low Gate Charge
- 100% EAS Guaranteed

Applications

- Load Switch
- DC/DC Converter



DDDD

Absolute Maximum Ratings (T_A = 25°C, unless otherwise noted)

Parameter		Symbol	Value	Unit	
Drain-Source Voltage		V _{DS}	-60	V	
Gate-Source Voltage		V _{GS}	±20	V	
Ocations Paris Comment	T _C = 25°C		-60	А	
Continuous Drain Current	T _C = 100°C	l _D	38		
Pulsed Drain Current ¹		Ірм	-240	А	
Single Pulse Avalanche Energy ²		EAS	180	mJ	
Total Power Dissipation	T _C = 25°C	P _D	89.3	W	
Operating Junction and Storage Temperature Range		Тл, Тата	-55 to 150	°C	

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ³	R _{0JA}	60	°C/W
Thermal Resistance from Junction-to-Case	Rелс	1.4	°C/W

Rev.1.0, 2024 Doc: W08031132 1/8



Electrical Characteristics (T_J = 25°C, unless otherwise noted)

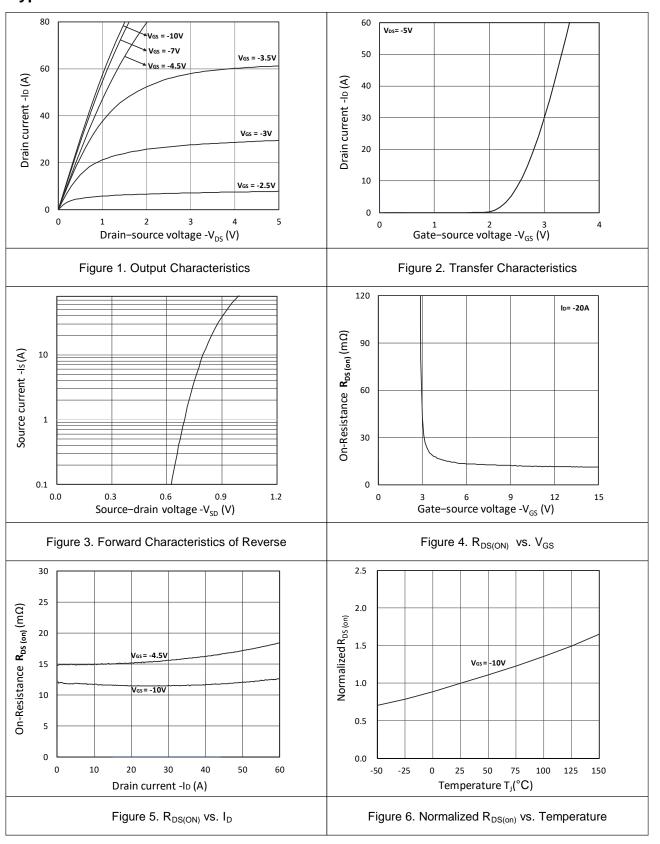
Parameter		Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static Characteristics								
Drain-Source Breakdown Voltage		V _{(BR)DSS}	V _{GS} = 0V, I _D = -250µA	-60	-	-	V	
Gate-body Leakage current		I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA	
Zero Gate Voltage Drain Current	T _J = 25°C	IDSS	V _{DS} = -60V, V _{GS} = 0V	-	-	-1	μΑ	
	T _J = 100°C			-	-	-100		
Gate-Threshold Voltage		V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	-1.2	-1.6	-2.0	V	
Drain-Source On-Resistance ⁴			V _{GS} = -10V, I _D = -20A	-	11.5	15.5	- mΩ	
		R _{DS(on)}	V _{GS} = -4.5V, I _D = -10A	-	15	20		
Forward Transconductance ⁴		G fs	V _{DS} = -10V, I _D = -20A	-	48	-	S	
Dynamic Characteristics ⁵								
Input Capacitance		Ciss		-	4610	-		
Output Capacitance Reverse Transfer Capacitance		Coss	V _{DS} = -30V, V _{GS} = 0V, f = 1MHz	-	266	-	pF	
		C _{rss}		-	227	-		
Gate Resistance		Rg	f = 1MHz	-	10.5	-	Ω	
Switching Characteristic	S ⁵							
Total Gate Charge		\mathbf{Q}_{g}		-	96.6	-	nC	
Gate-Source Charge		Q _{gs}	$V_{GS} = -10V, V_{DS} = -30V,$ $I_{D} = -20A$	-	9.2	-		
Gate-Drain Charge		Q_{gd}		-	14.8	-		
Turn-On Delay Time		t _{d(on)}		-	16	-		
Rise Time		tr	$V_{GS} = -10V, V_{DD} = -30V,$ $R_{G} = 3\Omega, I_{D} = -20A$	-	20	-	ns	
Turn-Off Delay Time		t _{d(off)}		-	80	-		
Fall Time		t _f		-	98	-		
Body Diode Reverse Recovery Time		t _{rr}		-	38	-	ns	
Body Diode Reverse Recovery Charge		Qrr	l _F = -20A, di/dt = -100A/μs	-	30	-	nC	
Drain-Source Body Diode Characteristics								
Diode Forward Voltage ⁴		V _{SD}	Is = -20A, V _{GS} = 0V	-	-	-1.2	V	
Continuous Source Current	T _C = 25°C	Is	-	-	-	-60	Α	

Notes:

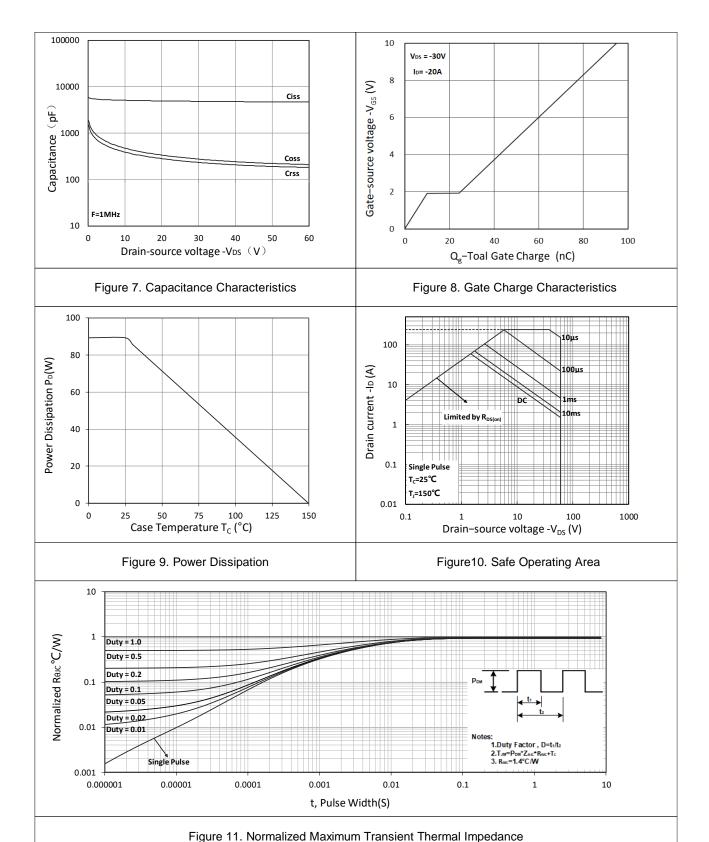
- 1. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$ =150°C.
- 2. The test condition is $V_{\text{DD}}\text{=-}25\text{V},\,V_{\text{GS}}\text{=-}10\text{V},\,L\text{=}0.4\text{mH},\,I_{\text{AS}}\text{=-}30\text{A}.$
- 3. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
- 4. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
- 5. This value is guaranteed by design hence it is not included in the production test.



Typical Characteristics









Test Circuit

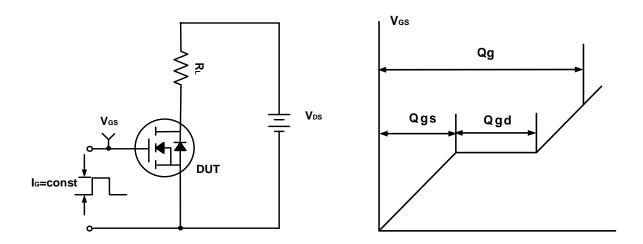


Figure A. Gate Charge Test Circuit & Waveforms

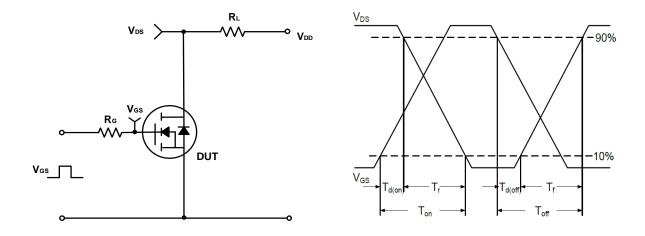


Figure B. Switching Test Circuit & Waveforms

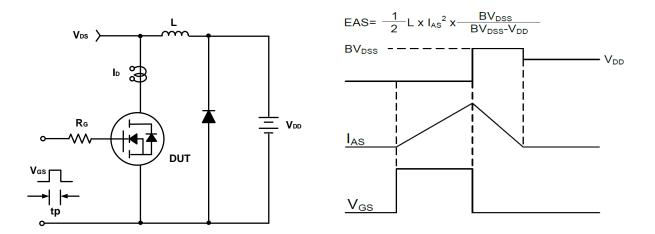
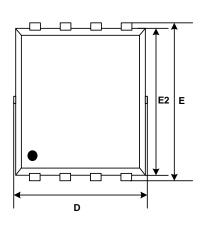


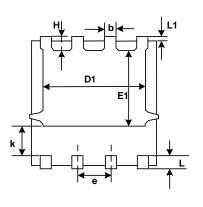
Figure C. Unclamped Inductive Switching Circuit & Waveforms

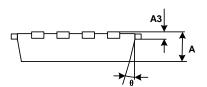


Mechanical Dimensions for PDFN5060-8L

COMMON DIMENSIONS







SYMBOL	MM			
	MIN	MAX		
Α	0.90	1.20		
А3	0.15	0.35		
D	4.80	5.40		
E	5.90	6.35		
D1	3.61	4.31		
E1	3.30	3.92		
E2	5.50	6.06		
k	1.10	•		
b	0.30	0.51		
е	1.27BSC			
L	0.38	0.71		
L1	0.05	0.36		
Н	0.38	0.71		
θ	0°	12°		



Ordering Information

Part	Package	Marking	Packing method
WMB65P06TS	PDFN5060-8L	B65P06S	Tape and Reel

Marking Information



B65P06S= Device code
WWXX XXX= Date code

Contact Information

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For additional information, please contact your local Sales Representative.

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Product Specification Statement

- 1. The product specification aims to provide users with a reference regarding various product parameters, performance, and usage. It presents certain aspects of the product's performance in graphical form and is intended solely for users to select product and make product comparisons, enabling users to better understand and evaluate the characteristics and advantages of the product. It does not constitute any commitment, warranty, or guarantee.
- 2. The product parameters described in the product specification are numerical values, characteristics, and functions obtained through actual testing or theoretical calculations of the product in an independent or ideal state. Due to the complexity of product applications and variations in test conditions and equipment, there may be slight fluctuations in parameter test values. WAYON shall not guarantee that the actual performance of the product when installed in the customer's system or equipment will be entirely consistent with the product specification, especially concerning dynamic parameters. It is recommended that users consult with professionals for product selection and system design. Users should also thoroughly validate and assess whether the actual parameters and performance when installed in their respective systems or equipment meet their requirements or expectations. Additionally, users should exercise caution in verifying product compatibility issues, and WAYON assumes no responsibility for the application of the product.
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