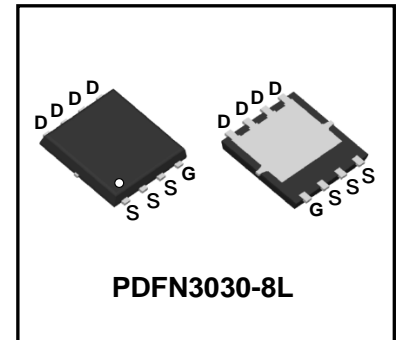


## 65V N-Channel Enhancement Mode Power MOSFET

### Description

WMQ140NV6LG4 uses Wayon's 4<sup>th</sup> generation power trench MOSFET technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance. This device is well suited for high efficiency fast switching applications.

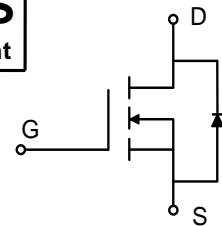


### Features

- $V_{DS} = 65V$ ,  $I_D = 33A$   
 $R_{DS(on)} < 14m\Omega @ V_{GS} = 10V$   
 $R_{DS(on)} < 19m\Omega @ V_{GS} = 4.5V$
- Green Device Available
- 100% EAS Guaranteed
- Low Gate Charge
- High Speed Switching



**RoHS**  
compliant



### Applications

- Power Management Switches
- DC/DC Converter

### Absolute Maximum Ratings ( $T_A = 25^\circ C$ , unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	65	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	$T_C = 25^\circ C$	33
		$T_C = 100^\circ C$	20
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	132	A
Single Pulse Avalanche Energy <sup>2</sup>	<b>EAS</b>	33.8	mJ
Total Power Dissipation	$P_D$	25.5	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^\circ C$

### Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient <sup>3</sup>	$R_{\theta JA}$	55	$^\circ C/W$
Thermal Resistance from Junction-to-Case	$R_{\theta JC}$	4.9	$^\circ C/W$

**Electrical Characteristics** ( $T_J = 25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	
<b>Static Characteristics</b>							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	65	-	-	V	
Gate-Body Leakage Current	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	$\pm 100$	nA	
Zero Gate Voltage Drain Current	$T_J=25^\circ\text{C}$	$I_{DSS}$	$V_{DS} = 65V, V_{GS} = 0V$	-	-	1	$\mu A$
	$T_J=100^\circ\text{C}$			-	-	100	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.2	1.7	2.2	V	
Drain-Source on-Resistance <sup>4</sup>	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 20A$	-	10.5	14	m $\Omega$	
		$V_{GS} = 4.5V, I_D = 10A$	-	14	19		
Forward Transconductance <sup>4</sup>	$g_{fs}$	$V_{DS} = 10V, I_D = 20A$	-	42	-	S	
<b>Dynamic Characteristics<sup>5</sup></b>							
Input Capacitance	$C_{iss}$	$V_{DS} = 30V, V_{GS} = 0V, f = 1MHz$	-	738	-	pF	
Output Capacitance	$C_{oss}$		-	225	-		
Reverse Transfer Capacitance	$C_{rss}$		-	13.5	-		
Gate Resistance	$R_G$	$f = 1MHz$	-	1.2	-	$\Omega$	
<b>Switching Characteristics<sup>5</sup></b>							
Total Gate Charge	$Q_g$	$V_{GS} = 10V, V_{DS} = 30V, I_D = 20A$	-	14	-	nC	
Gate-Source Charge	$Q_{gs}$		-	2.6	-		
Gate-Drain Charge	$Q_{gd}$		-	2.9	-		
Turn-on Delay Time	$t_{d(on)}$	$V_{GS} = 10V, V_{DD} = 30V, R_G = 3\Omega, I_D = 20A$	-	5.7	-	ns	
Rise Time	$t_r$		-	5.1	-		
Turn-off Delay Time	$t_{d(off)}$		-	14.8	-		
Fall Time	$t_f$		-	4.6	-		
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 20A, di/dt = 100A/\mu s$	-	24	-	ns	
Body Diode Reverse Recovery Charge	$Q_{rr}$		-	9.6	-	nC	
<b>Drain-Source Body Diode Characteristics</b>							
Diode Forward Voltage <sup>4</sup>	$V_{SD}$	$I_S = 20A, V_{GS} = 0V$	-	-	1.2	V	
Continuous Source Current	$T_C=25^\circ\text{C}$	$I_S$	-	-	33	A	

## Notes:

1. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^\circ\text{C}$ .
2. The test condition is  $V_{DD}=25V, V_{GS}=10V, L=0.4mH, I_{AS}=13A$ .
3. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .
5. This value is guaranteed by design hence it is not included in the production test.

Typical Characteristics

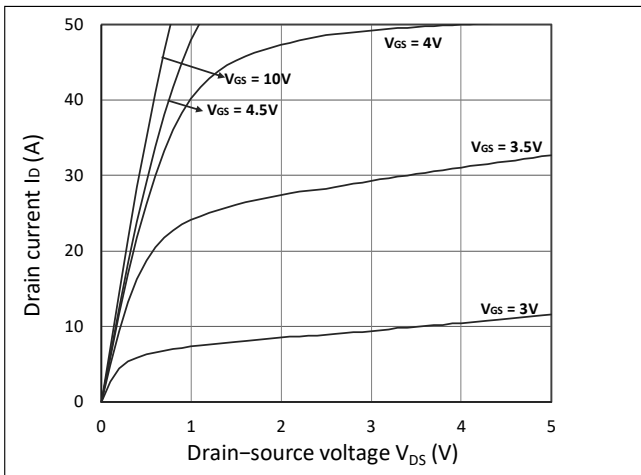


Figure 1. Output Characteristics

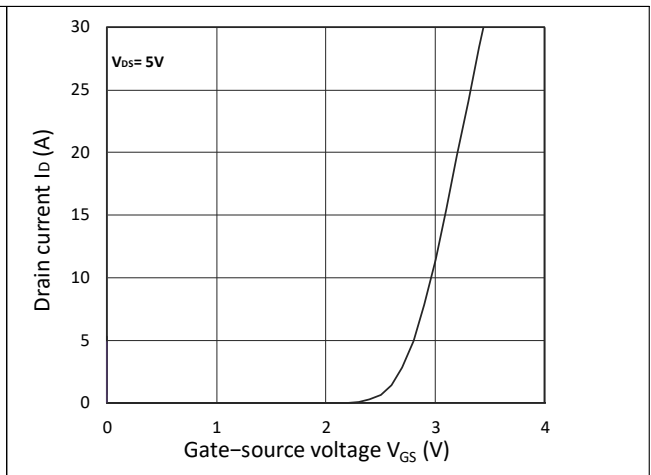


Figure 2. Transfer Characteristics

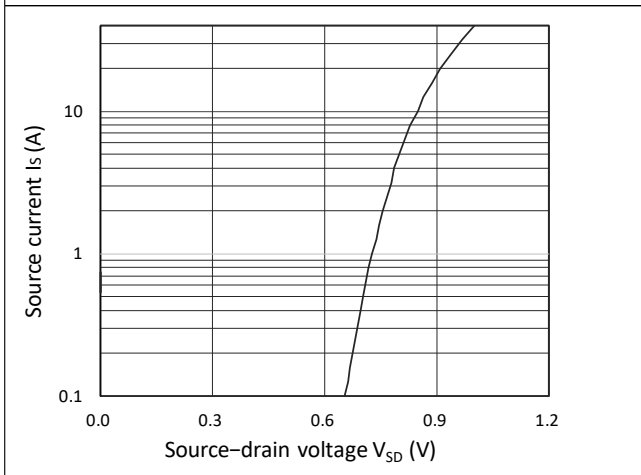


Figure 3. Forward Characteristics of Reverse

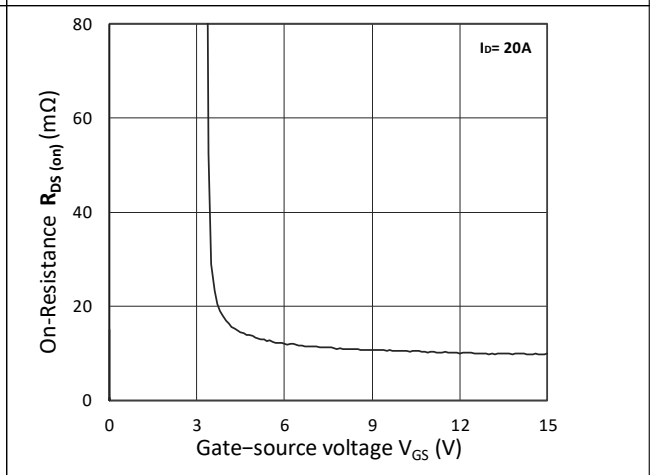


Figure 4.  $R_{DS(ON)}$  vs.  $V_{GS}$

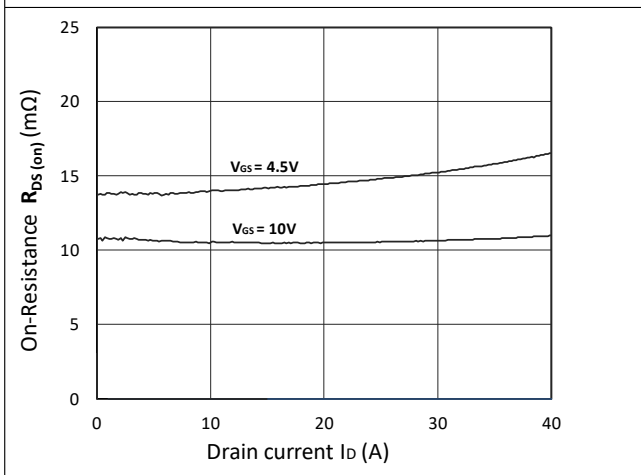


Figure 5.  $R_{DS(ON)}$  vs.  $I_D$

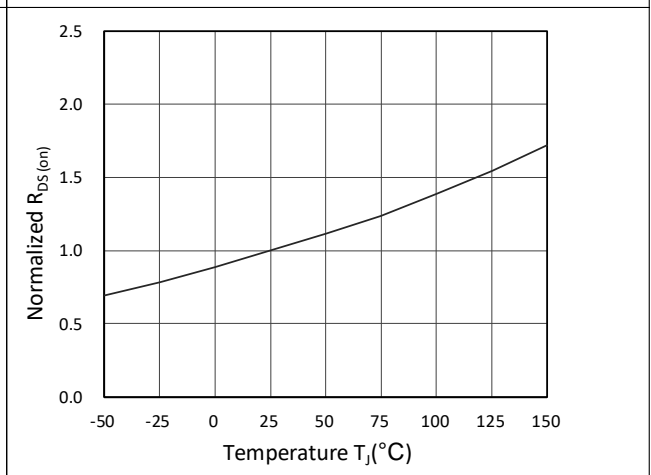


Figure 6. Normalized  $R_{DS(ON)}$  vs. Temperature

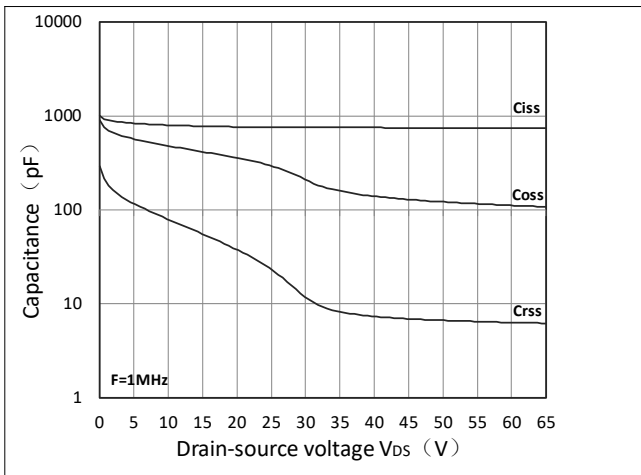


Figure 7. Capacitance Characteristics

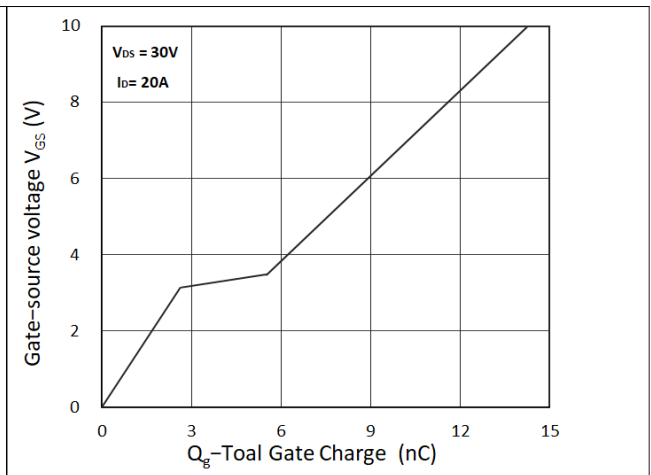


Figure 8. Gate Charge Characteristics

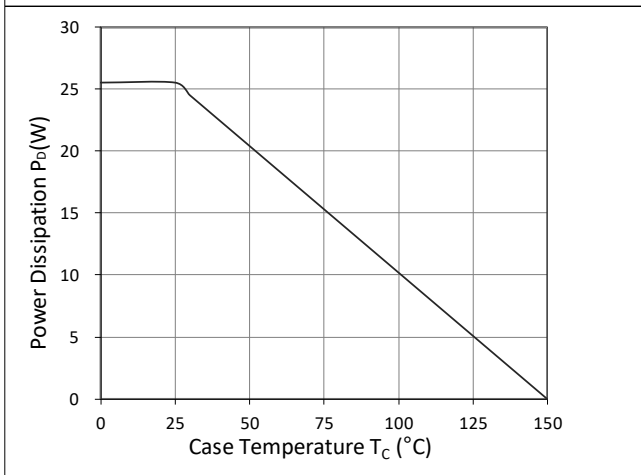


Figure 9. Power Dissipation

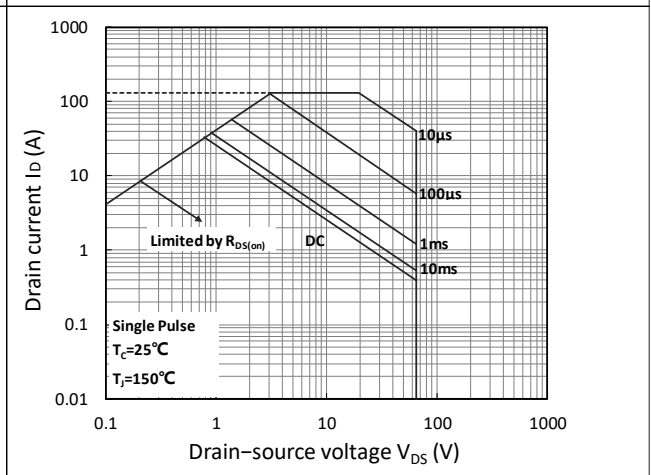


Figure 10. Safe Operating Area

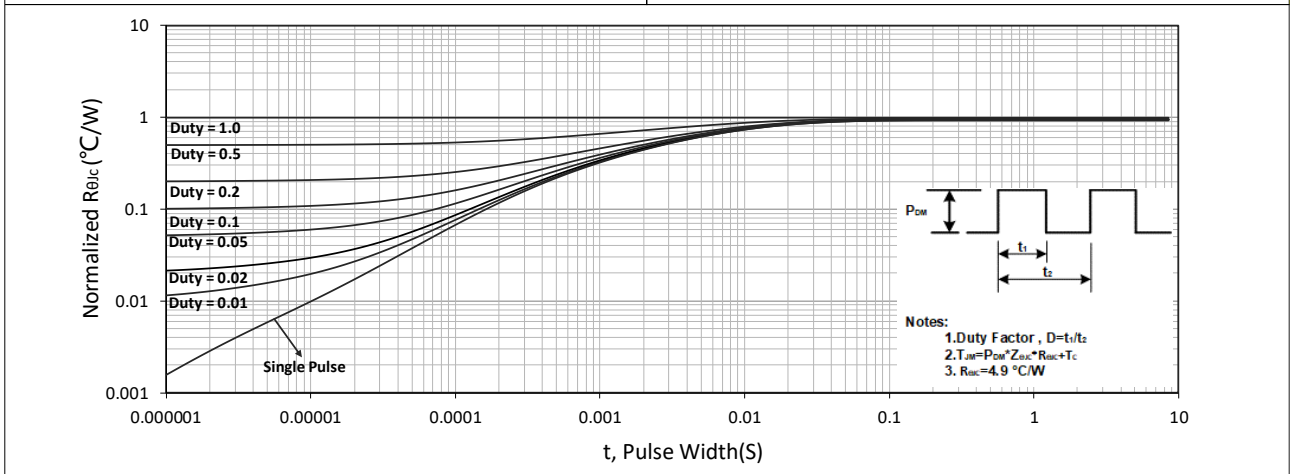


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

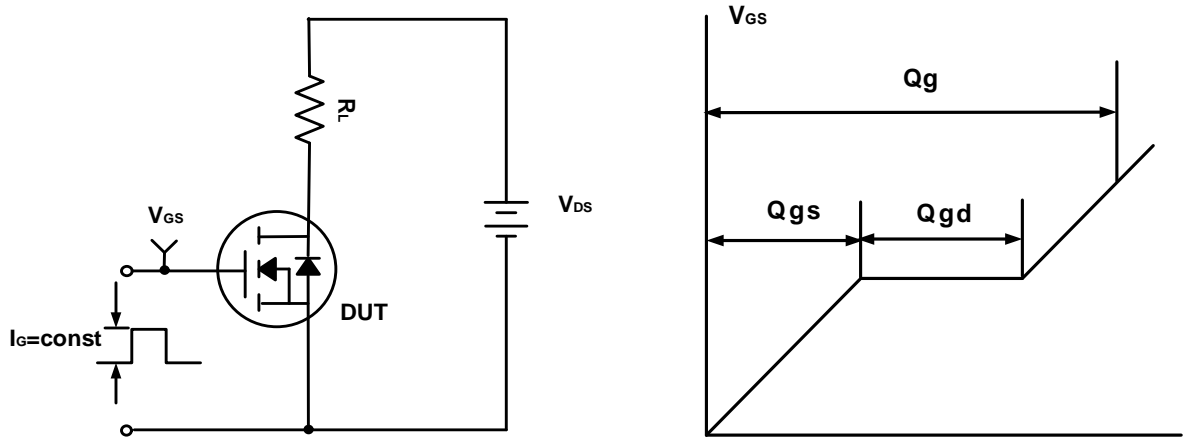


Figure A. Gate Charge Test Circuit & Waveforms



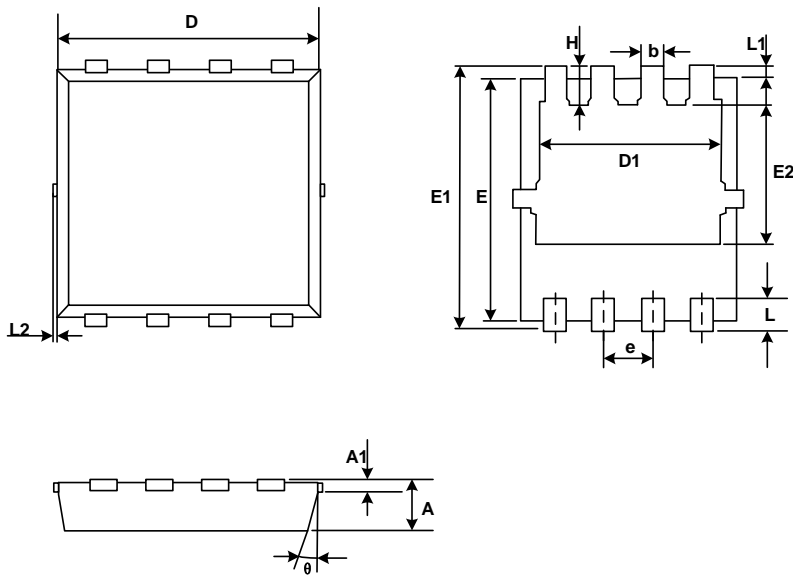
Figure B. Switching Test Circuit & Waveforms



Figure C. Unclamped Inductive Switching Circuit & Waveforms

Mechanical Dimensions for PDFN3030-8L

COMMON DIMENSIONS

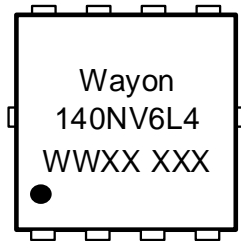


SYMBOL	MM	
	MIN	MAX
A	0.65	0.90
A1	0.10	0.25
D	2.90	3.25
D1	2.25	2.69
E	2.90	3.20
E1	3.00	3.60
E2	1.35	2.20
b	0.20	0.40
e	0.65BSC	
L	0.15	0.50
L1	0.13BSC	
L2	0.00	0.20
H	0.15	0.65
$\theta$	0°	14°

## Ordering Information

Part	Package	Marking	Packing method
WMQ140NV6LG4	PDFN3030-8L	140NV6L4	Tape and Reel

## Marking Information



140NV6L4 = Device code

WWXX XXX= Date code


## Contact Information

No.1001, Shiwan(7) Road, Pudong District, Shanghai, P.R.China.201207

Tel: 86-21-50310888 Fax: 86-21-50757680 Email: market@way-on.com

WAYON website: <http://www.way-on.com>

For additional information, please contact your local Sales Representative.

 is registered trademarks of Wayon Corporation.

## Disclaimer

WAYON reserves the right to make changes without further notice to any Products herein to improve reliability, function, or design. The Products are not designed for use in hostile environments, including, without limitation, aircraft, nuclear power generation, medical appliances, and devices or systems in which malfunction of any Product can reasonably be expected to result in a personal injury. The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. WAYON does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Products or technical information described in this document.